

FIG. 1

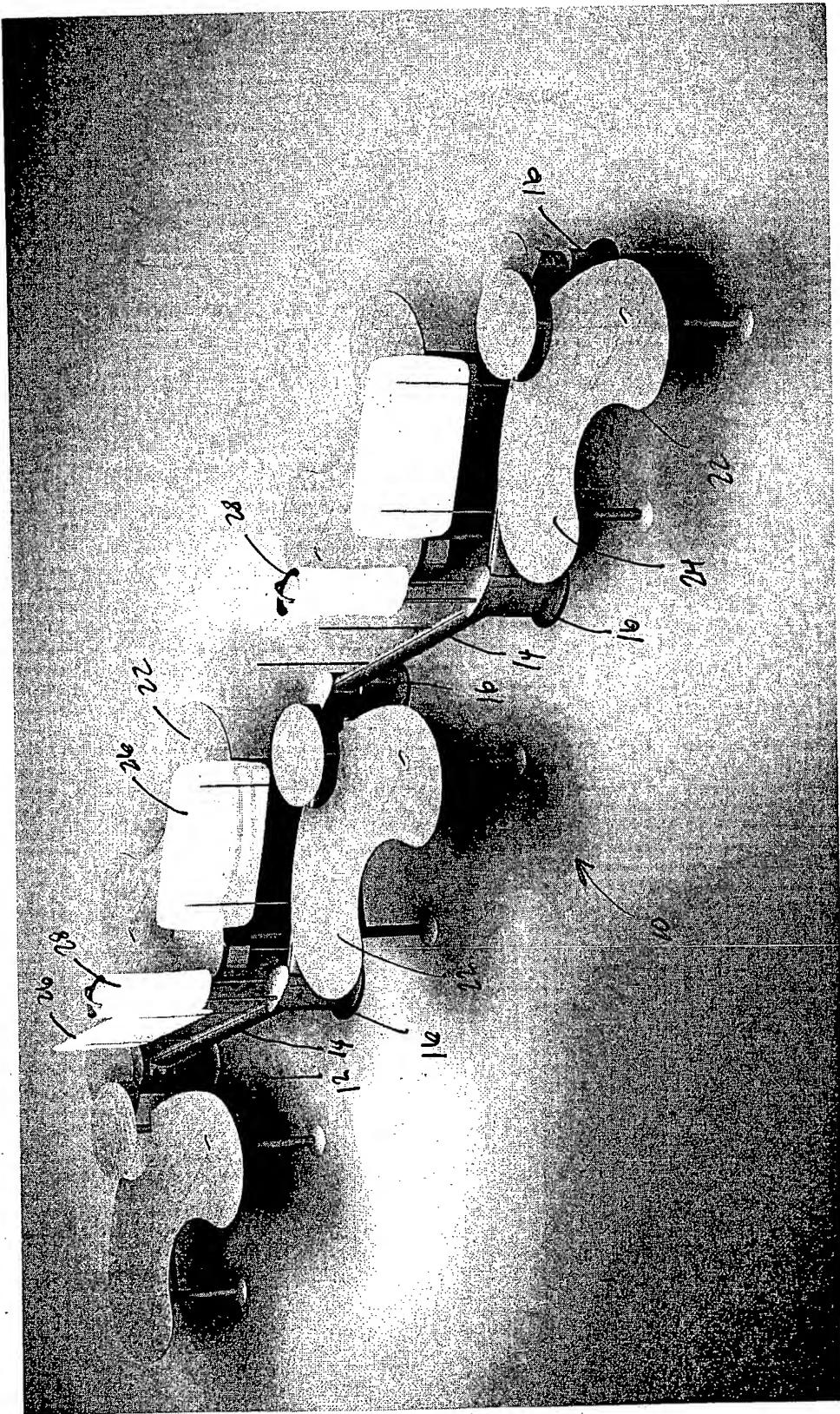
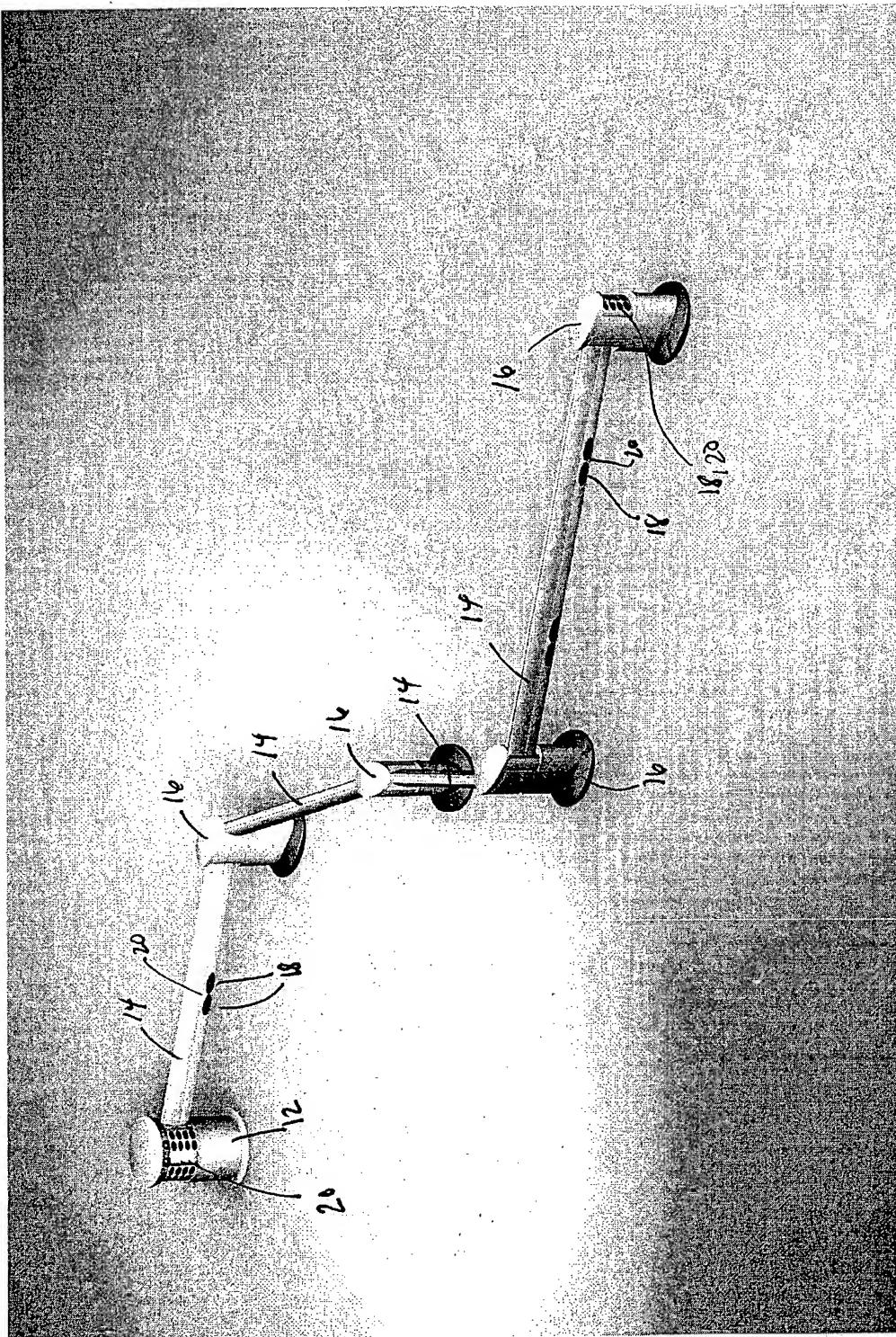


FIG. 2



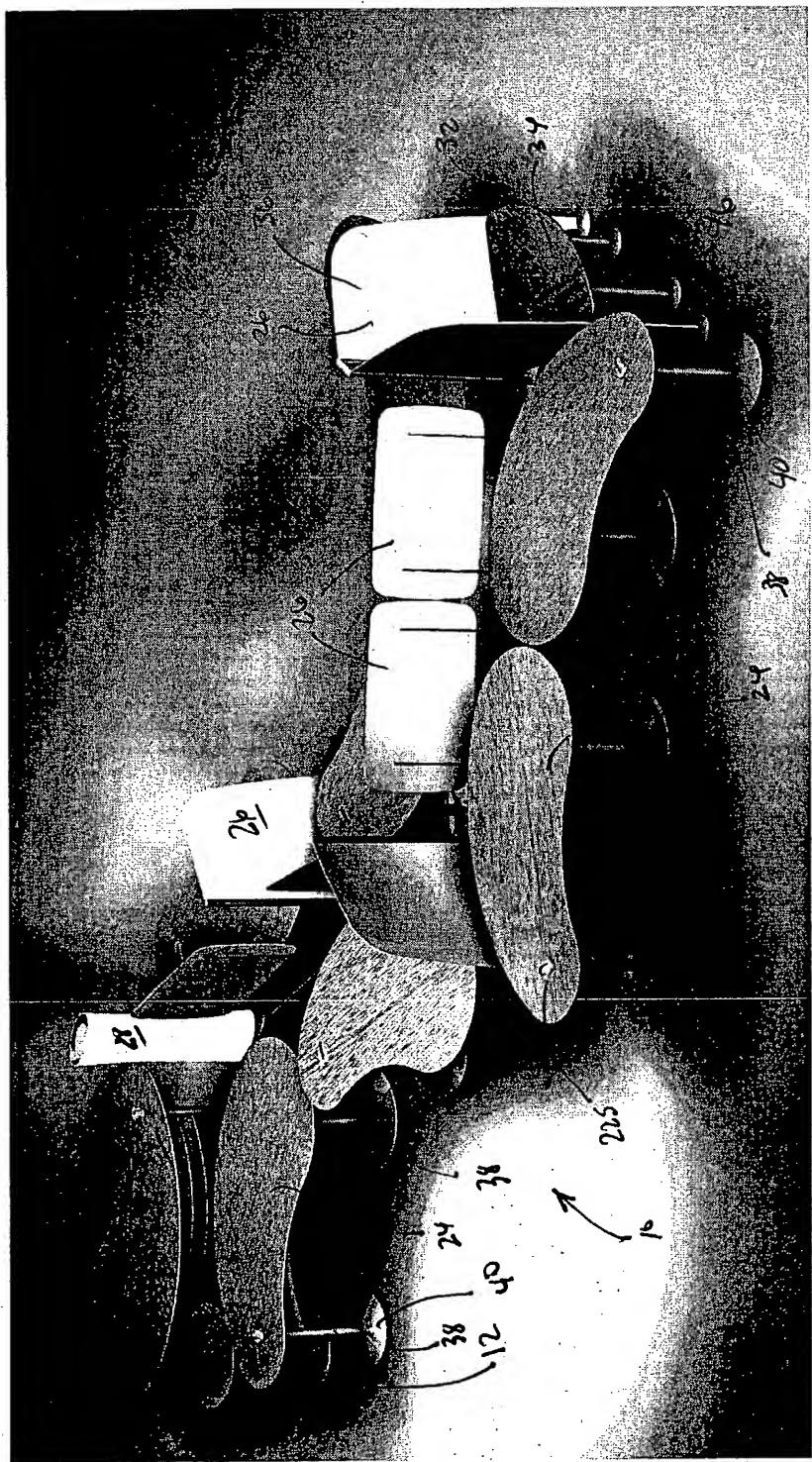


FIG. 3

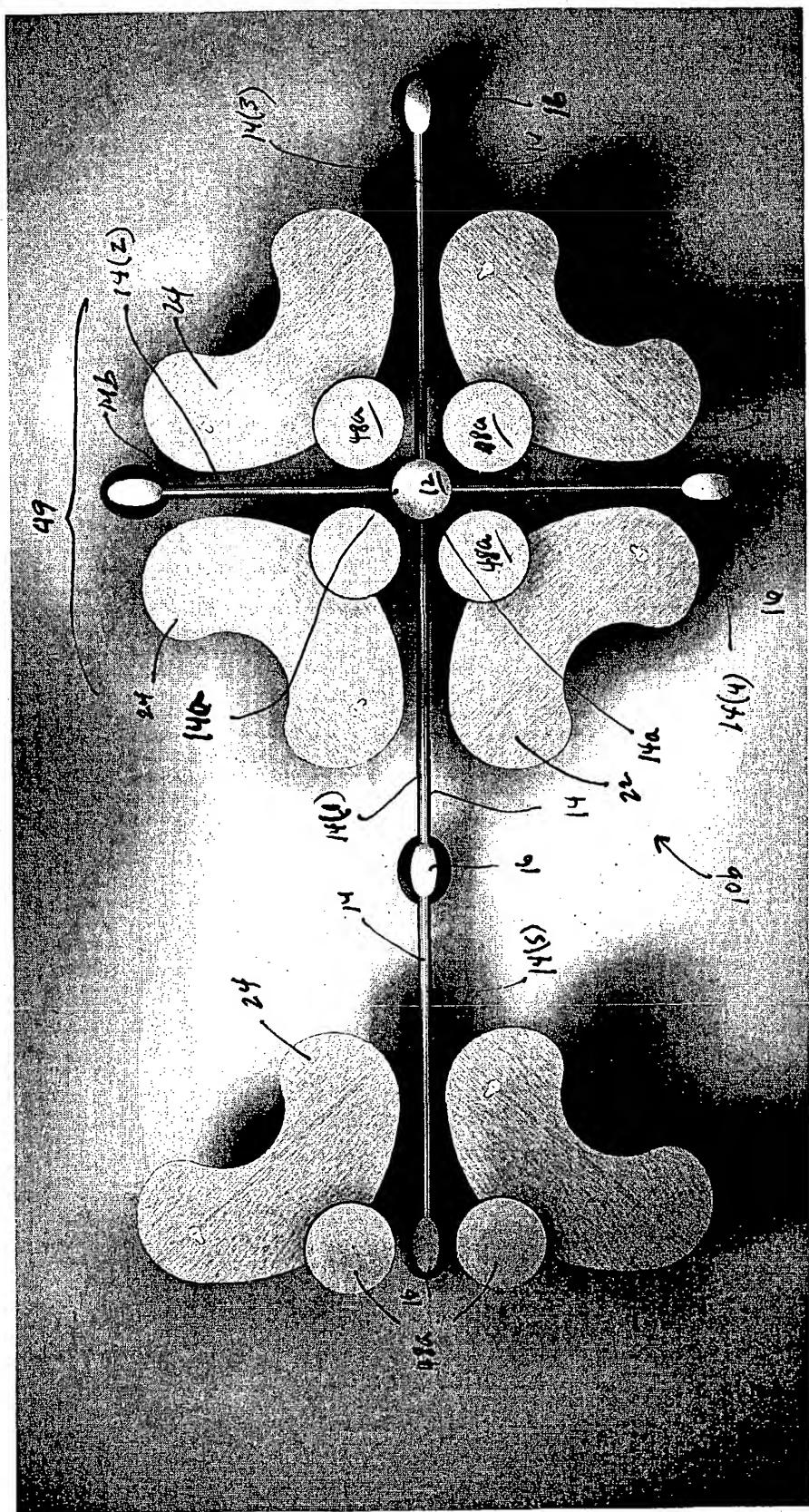


FIG. 4

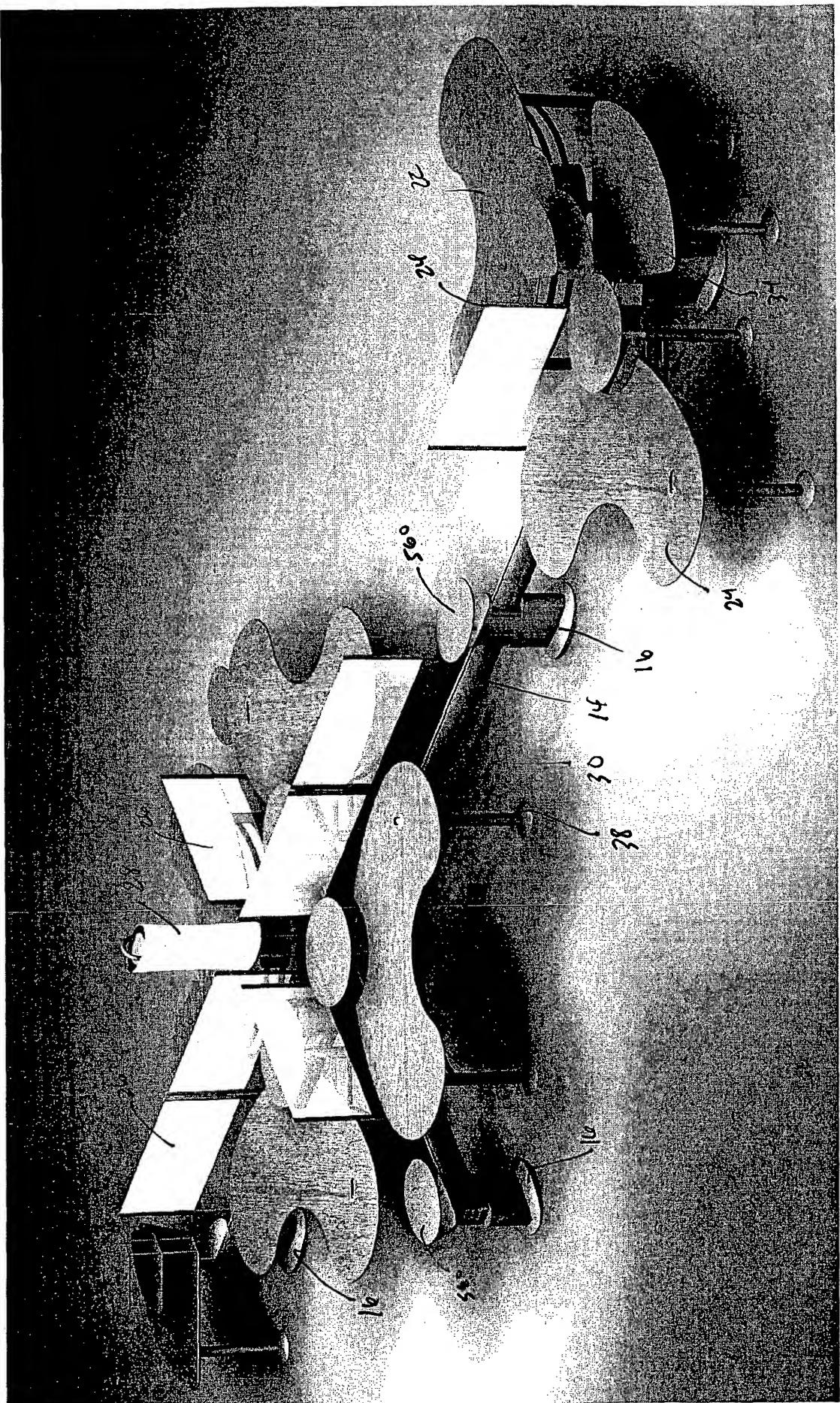


FIG. 5

FIG. 6

● = Substation    — = Channel    ● = Channel Support

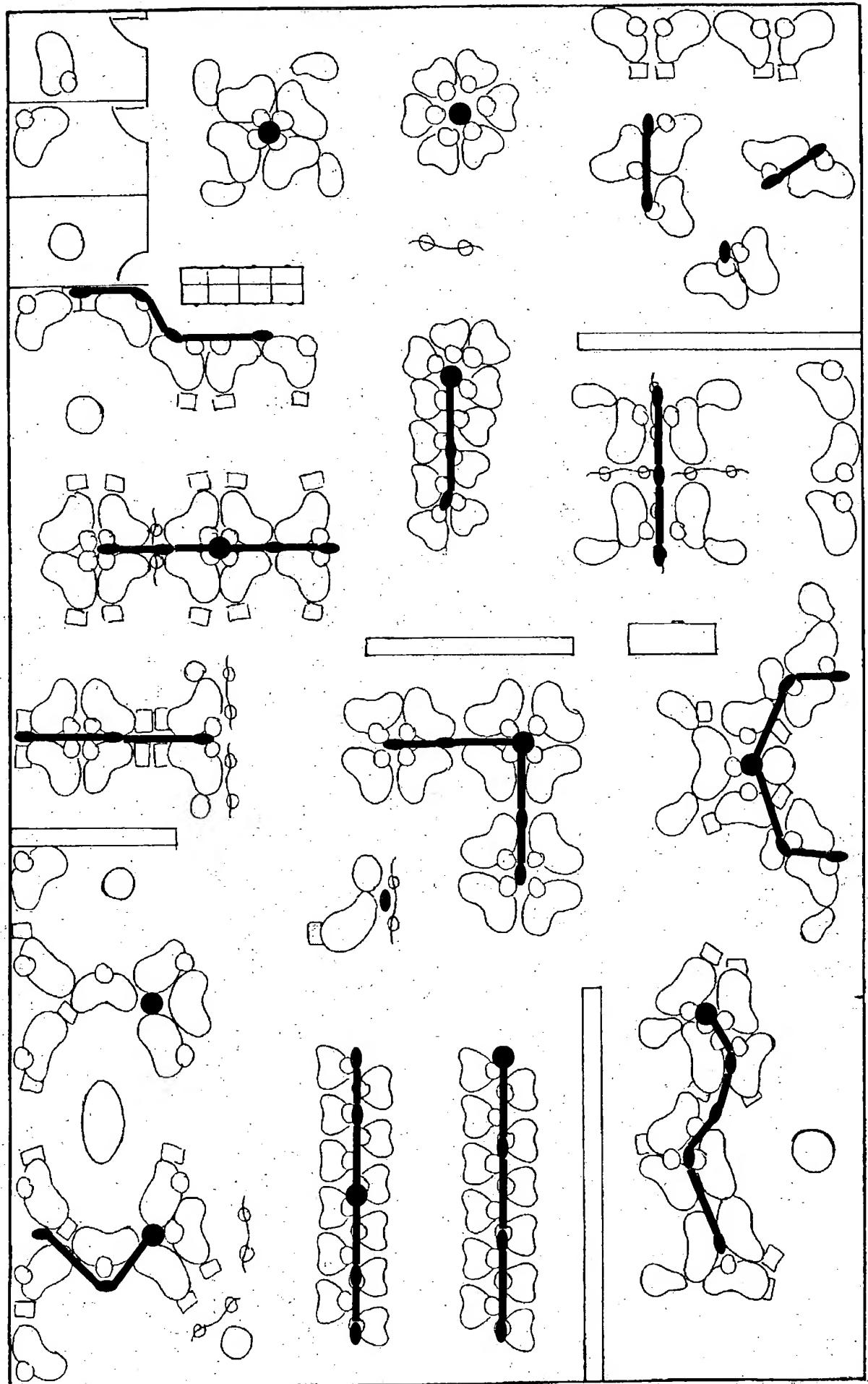
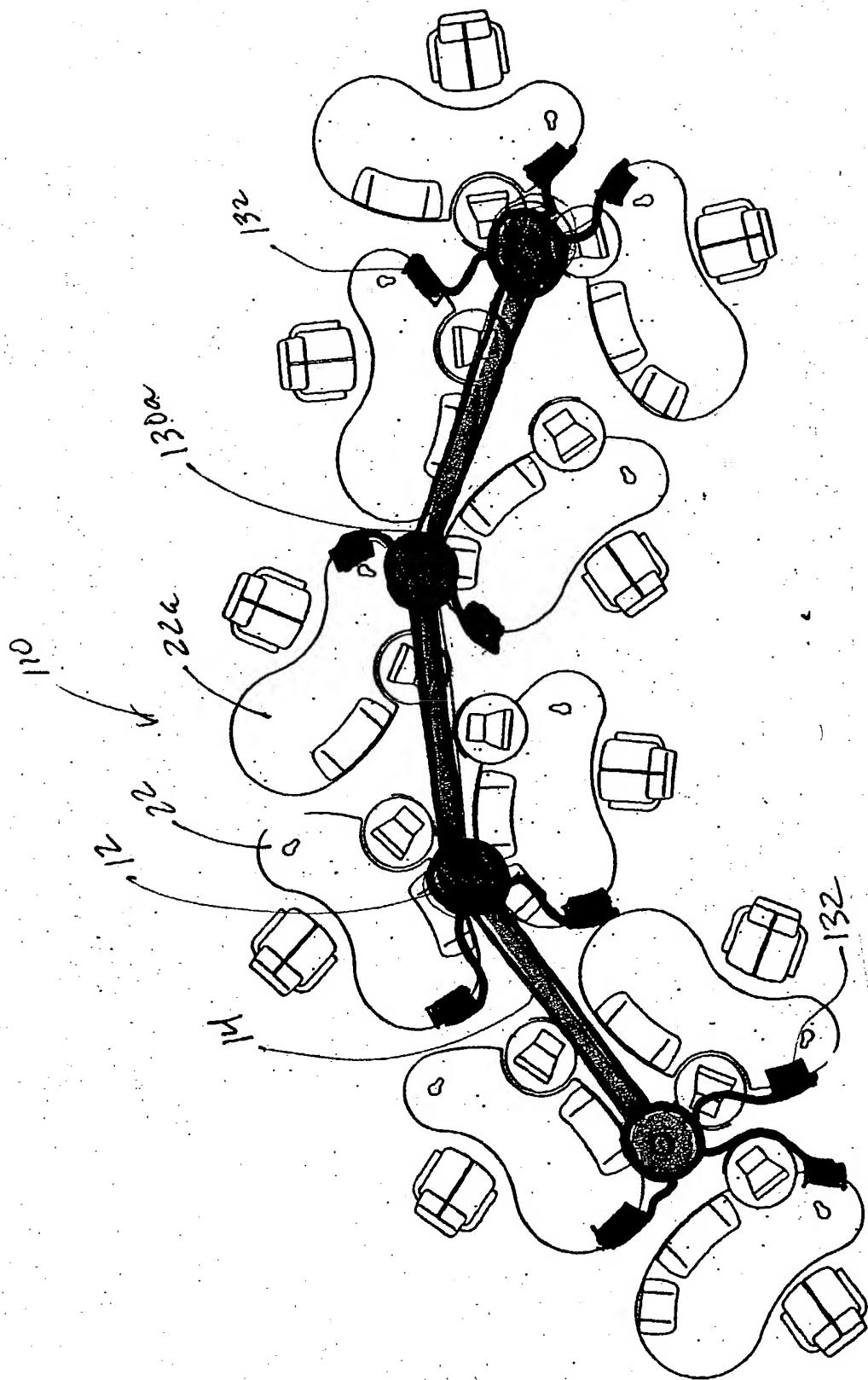


FIG. 7



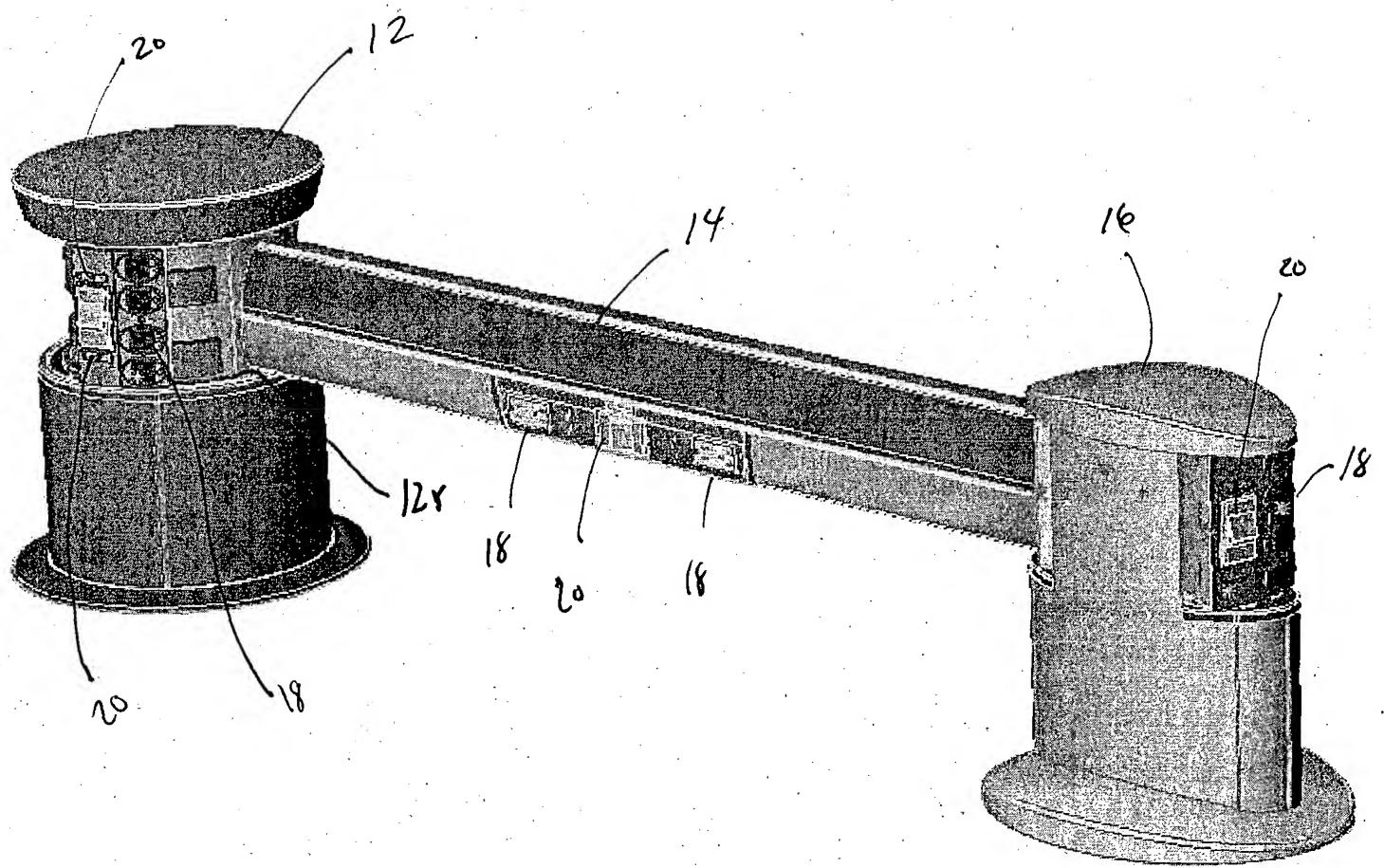


FIG. 8

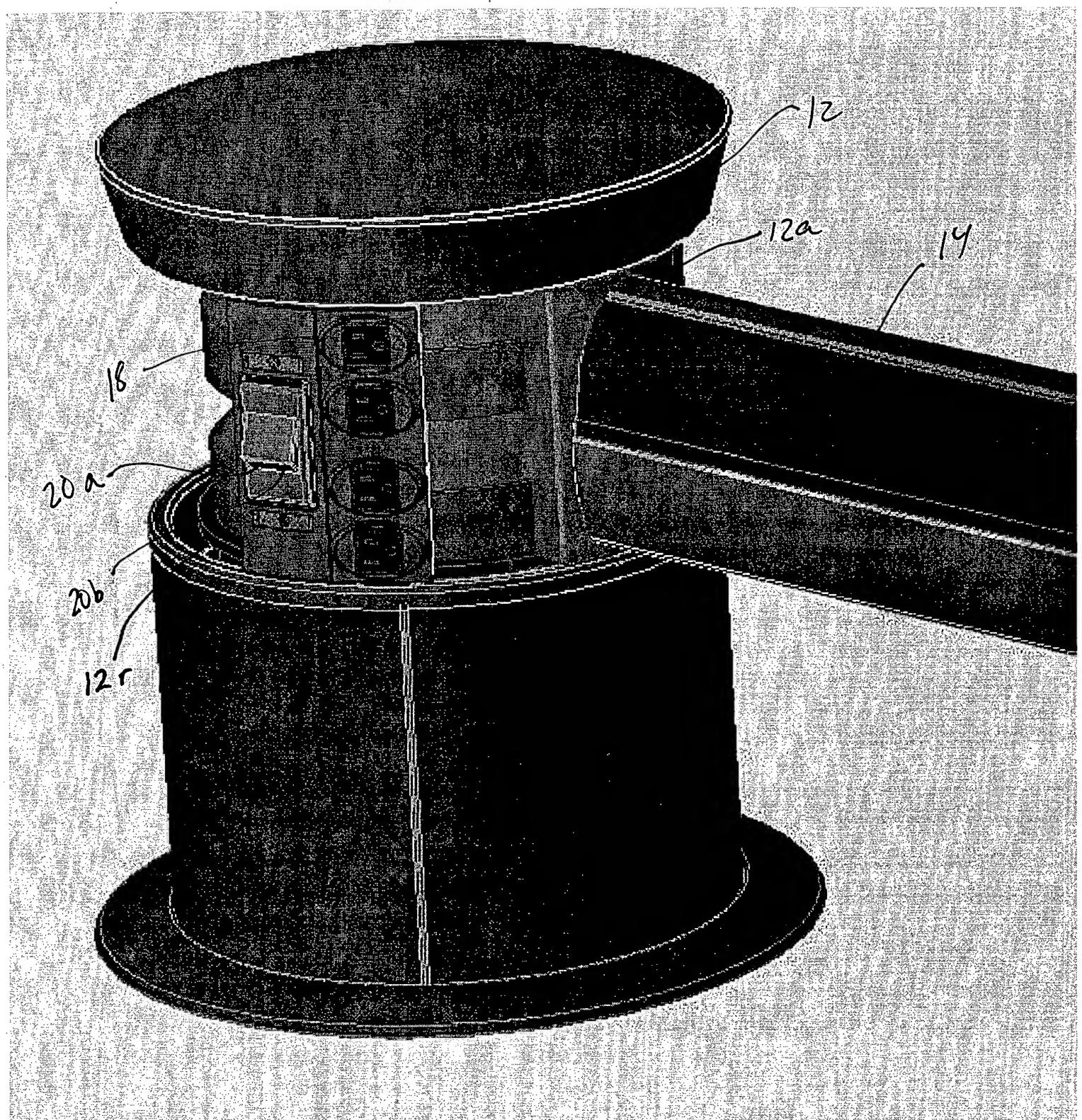


FIG. 9

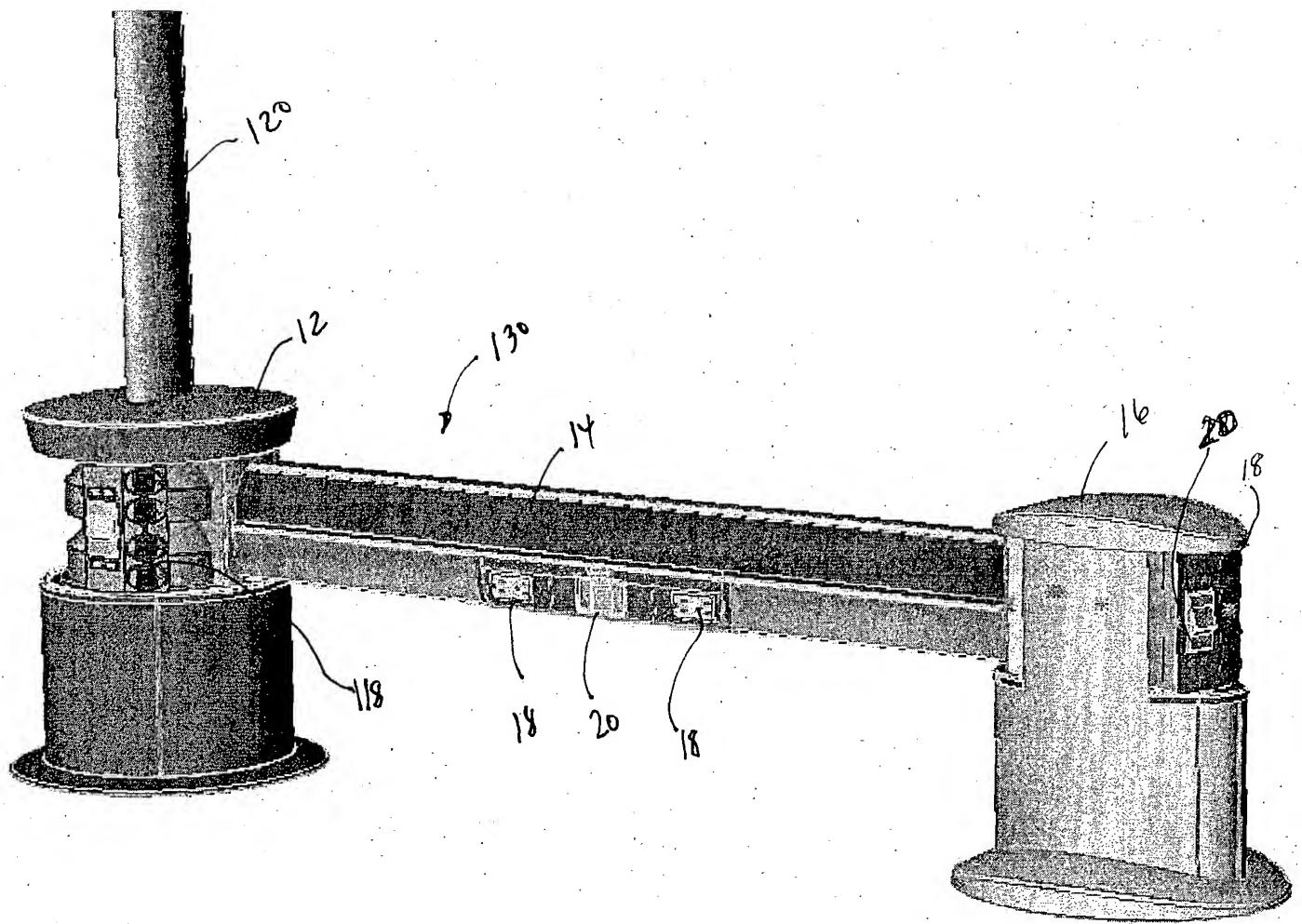


FIG. 10

FIG. 11a

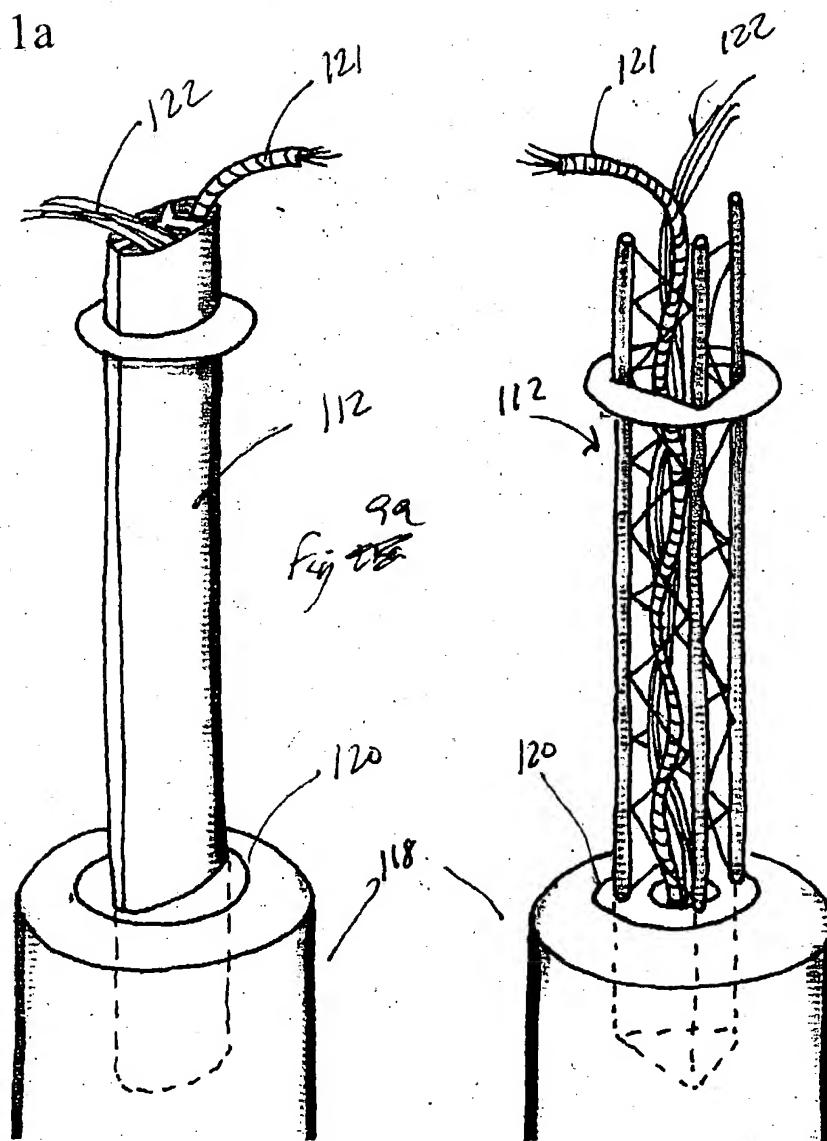


FIG. 11b

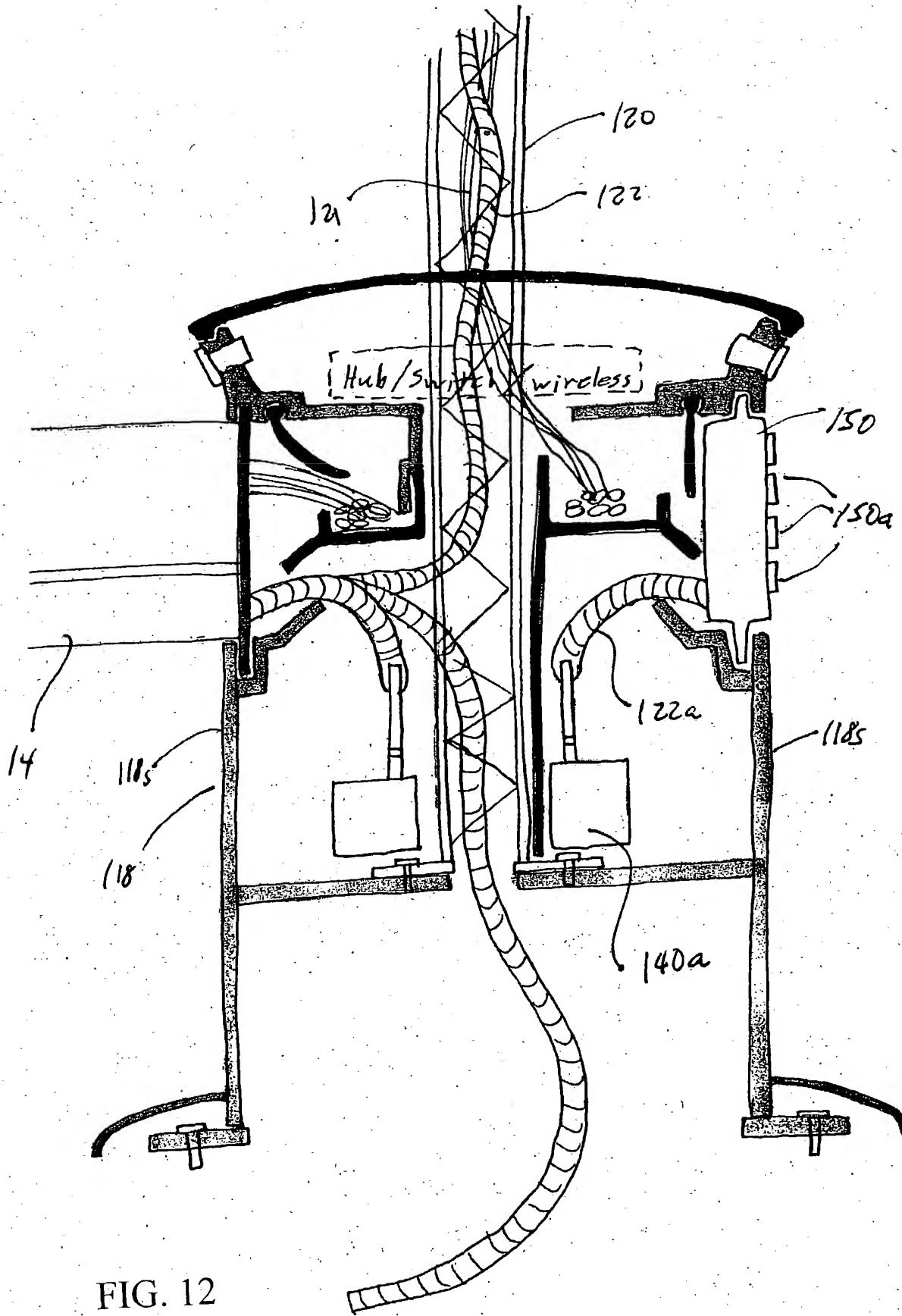


FIG. 12

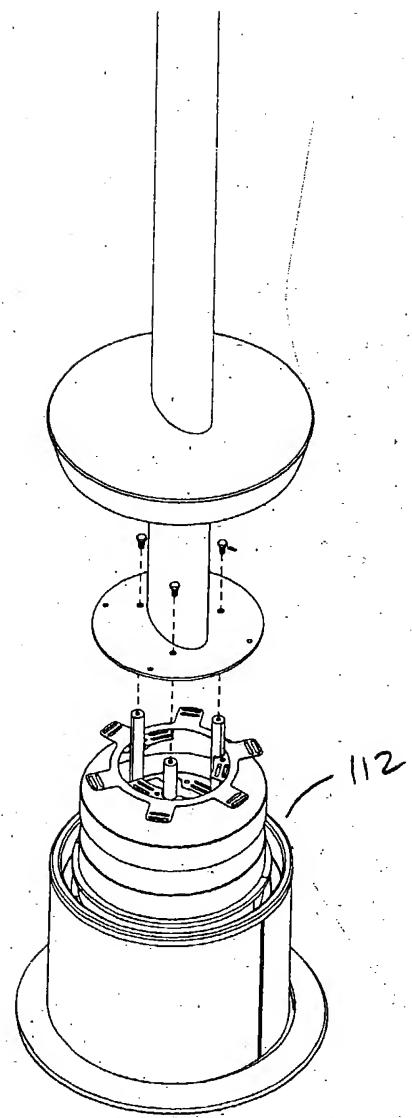


FIG. 12a

FIG. 13a

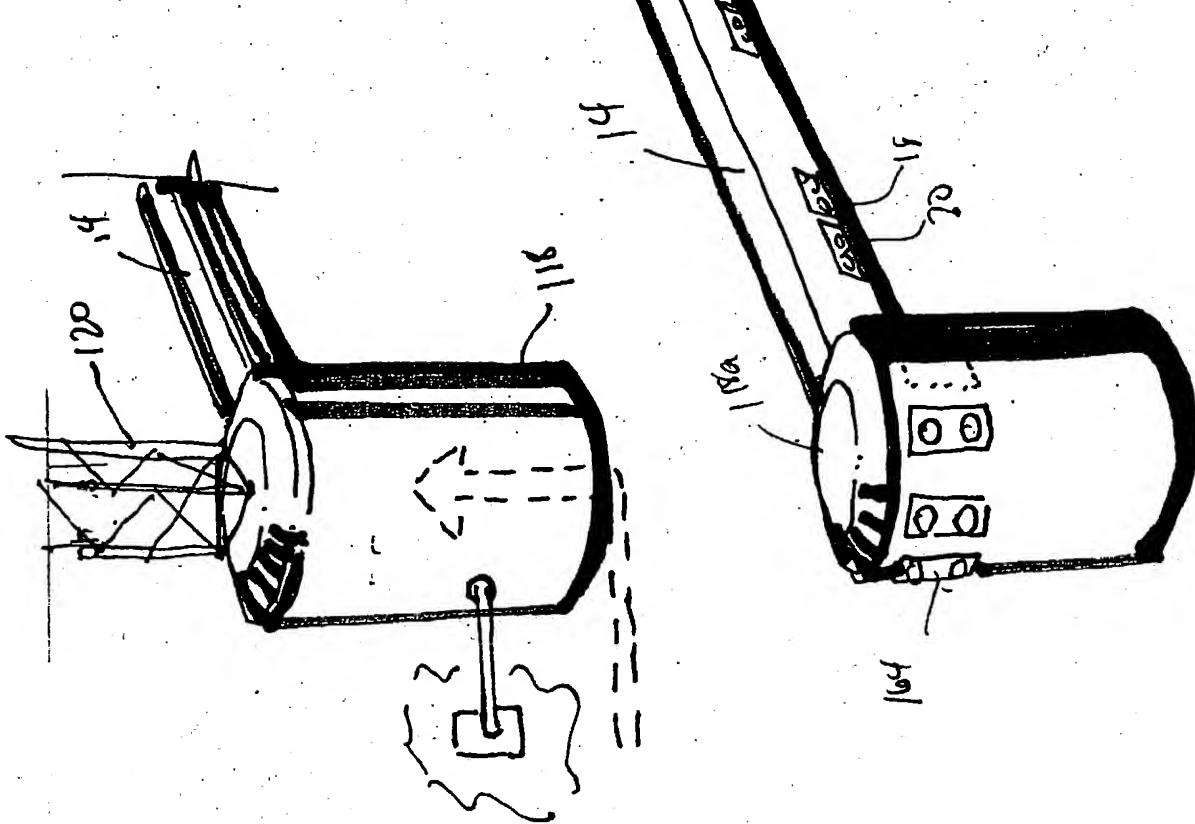


FIG. 13b

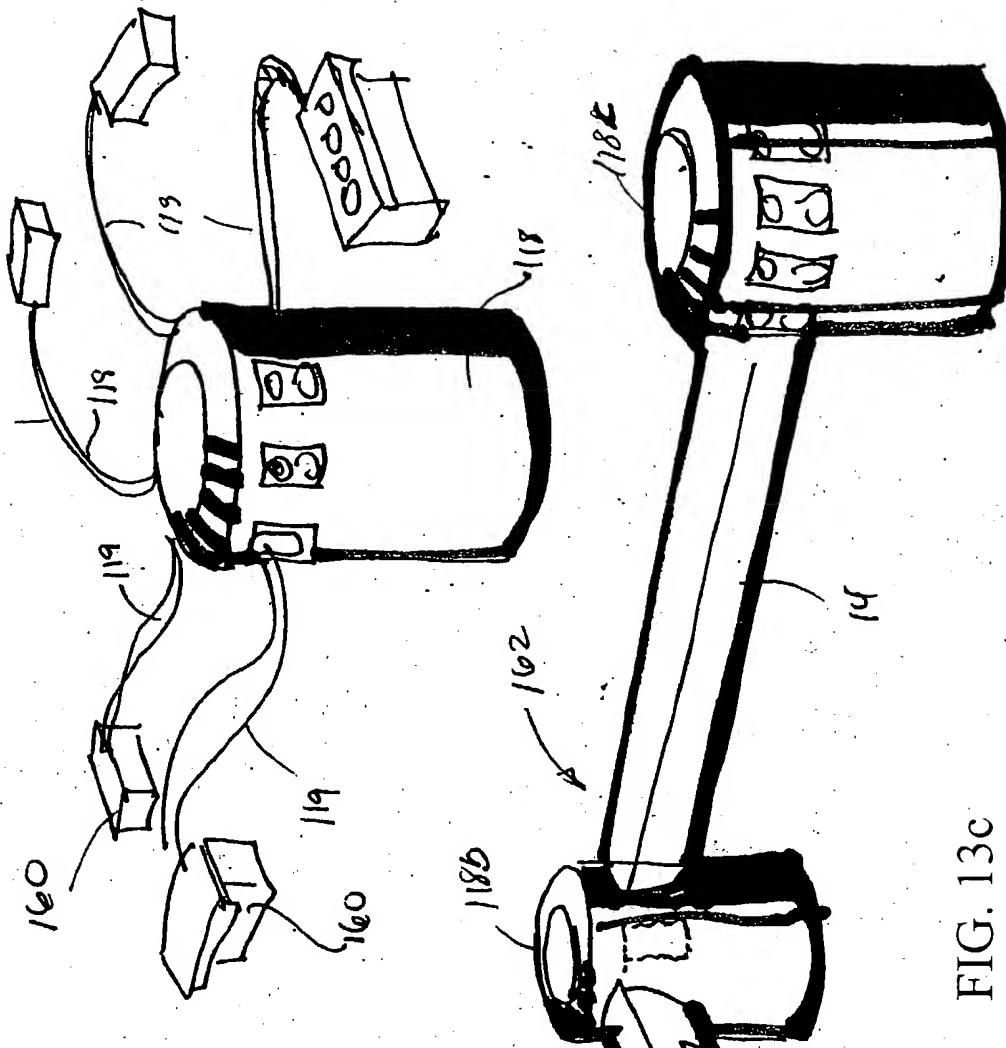


FIG. 13C.

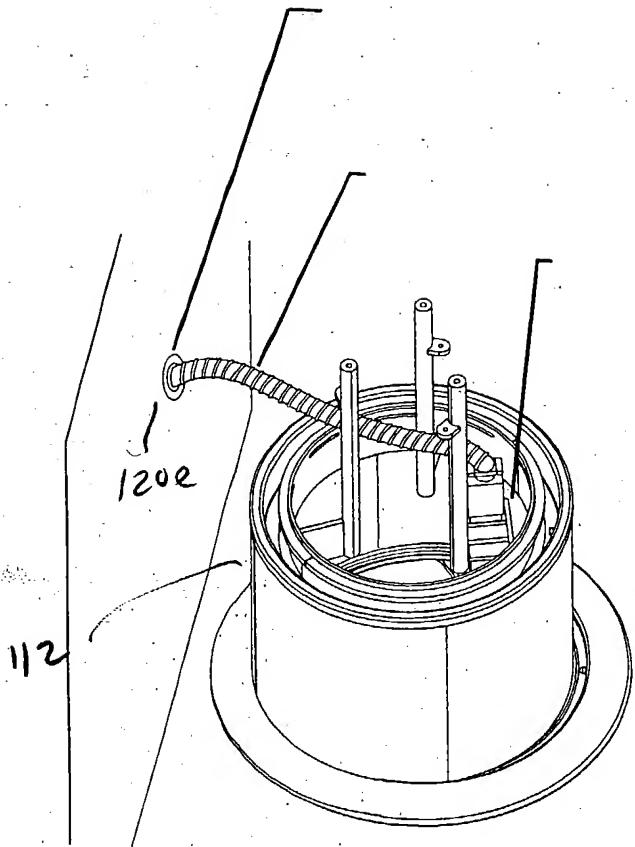


FIG. 13d

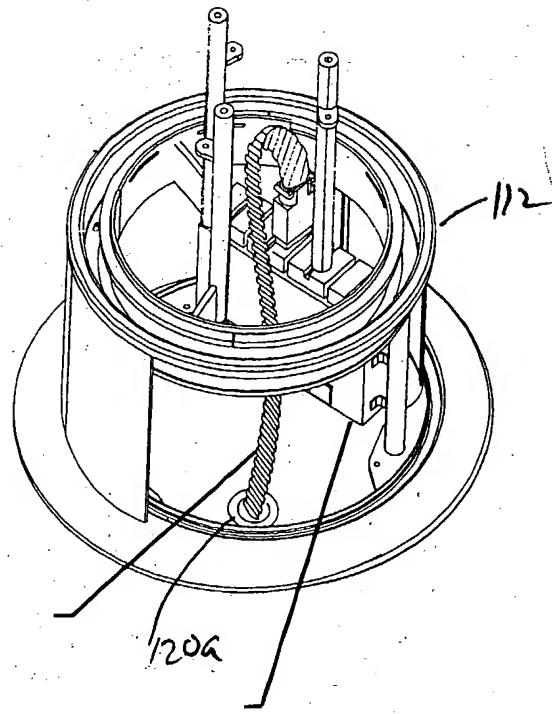
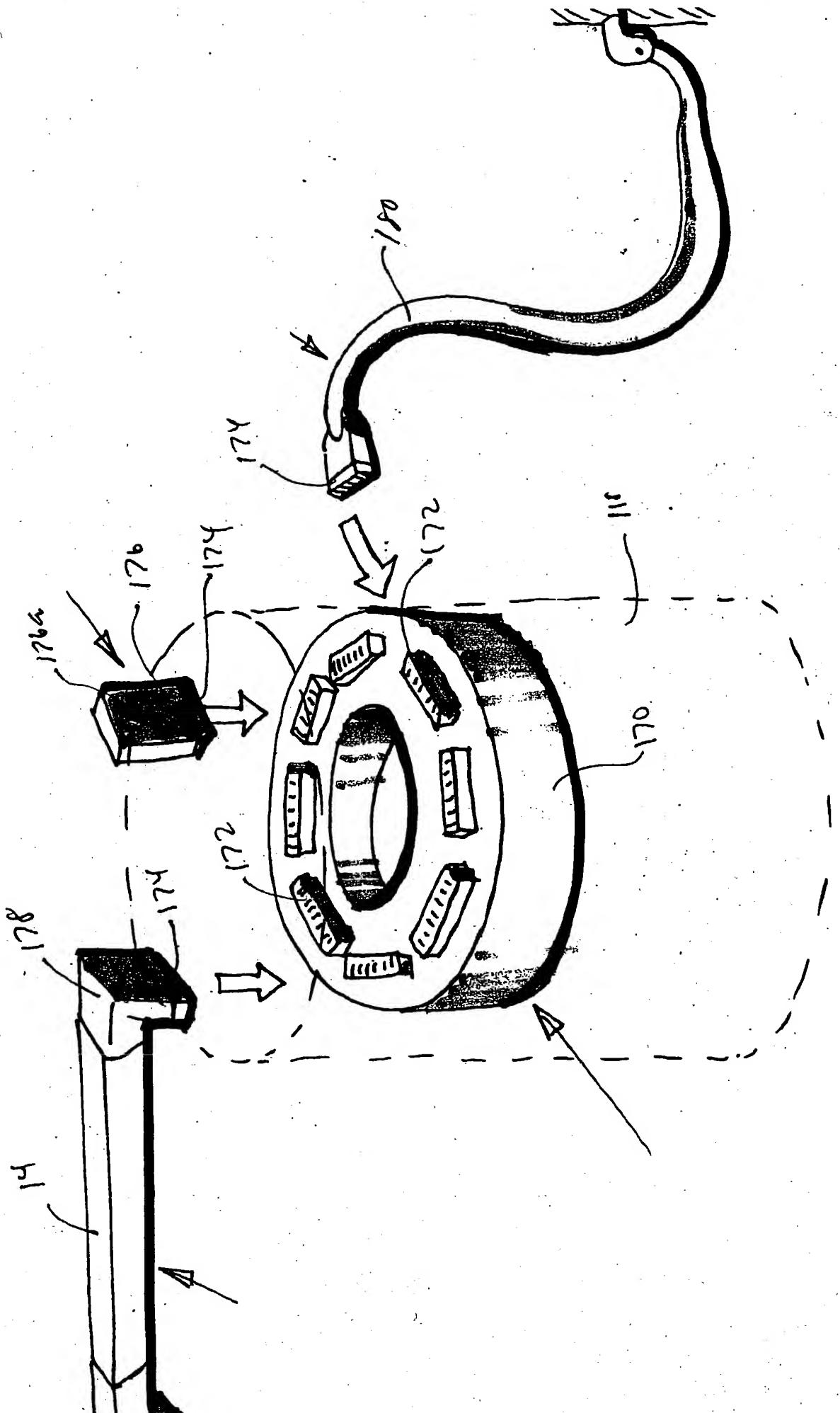


FIG. 13e

FIG. 14



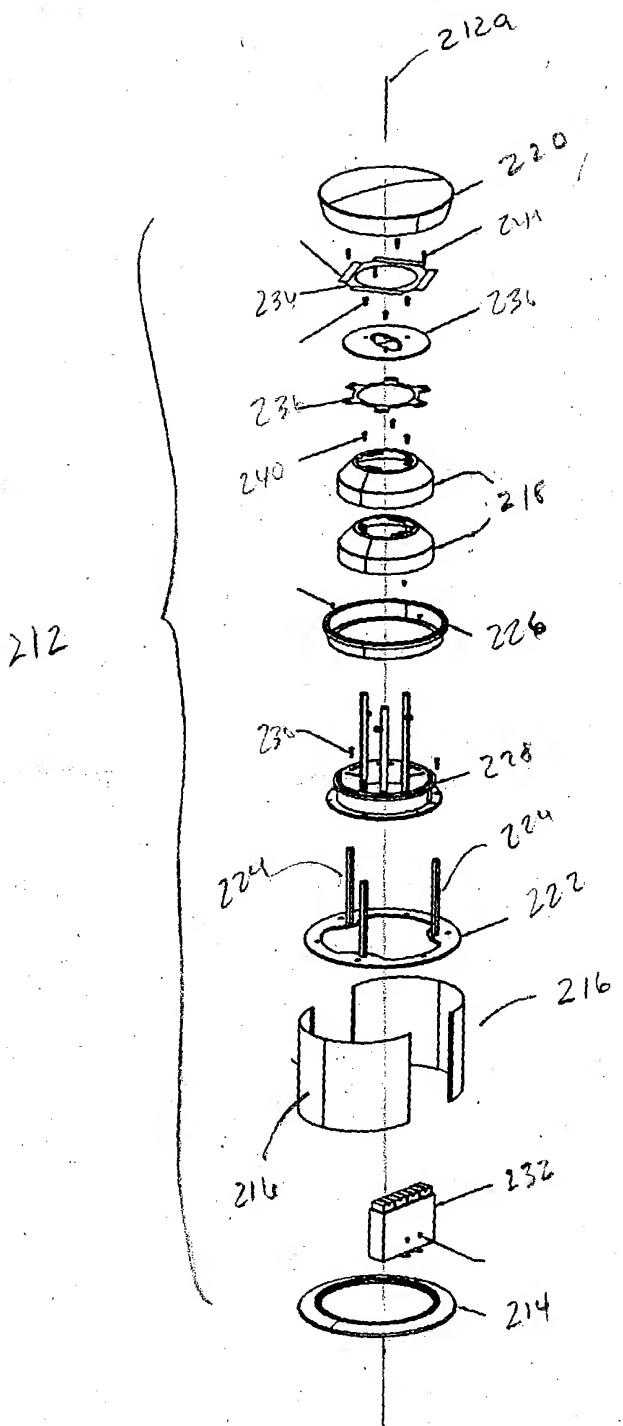


FIG. 15

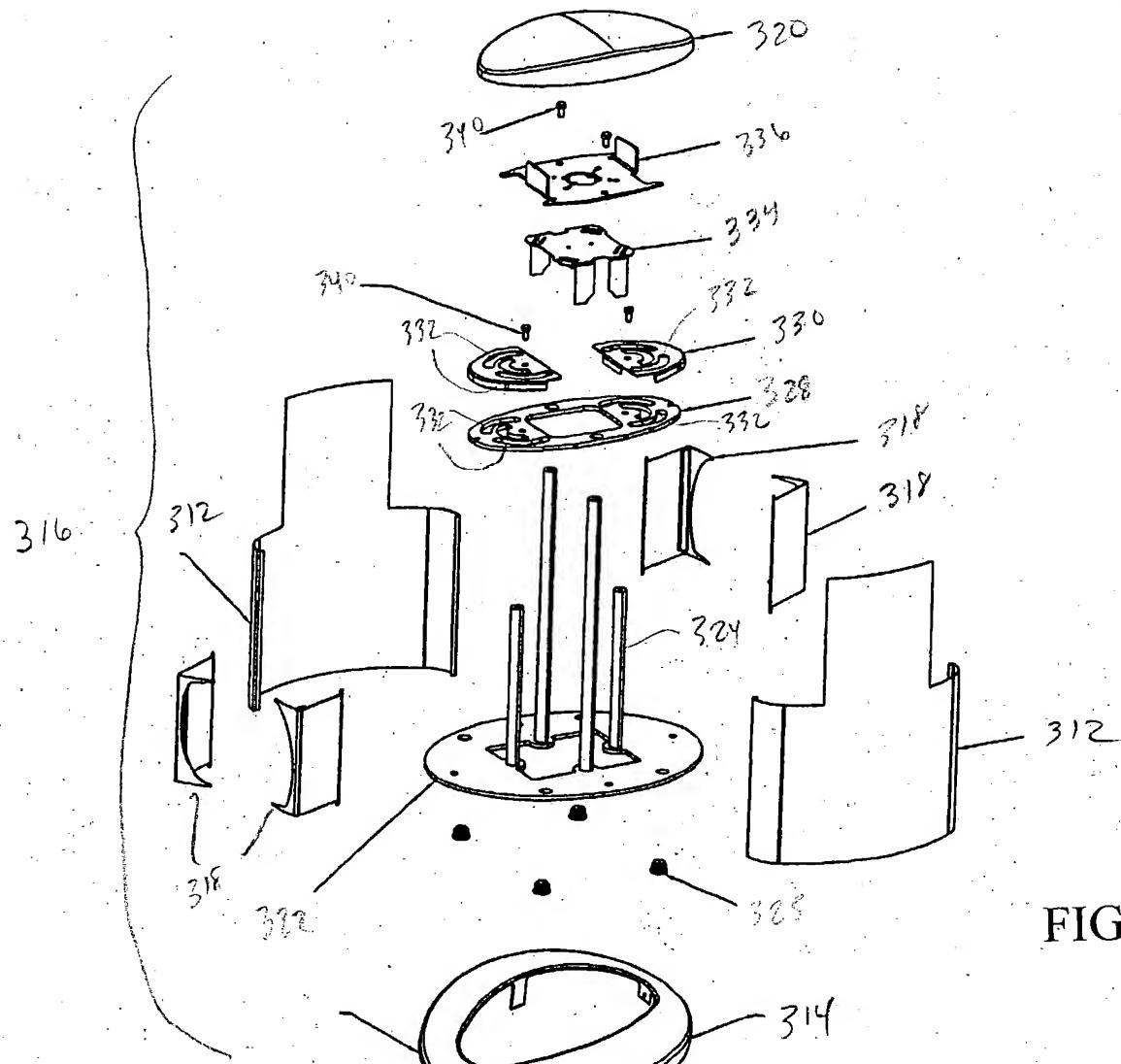


FIG. 16

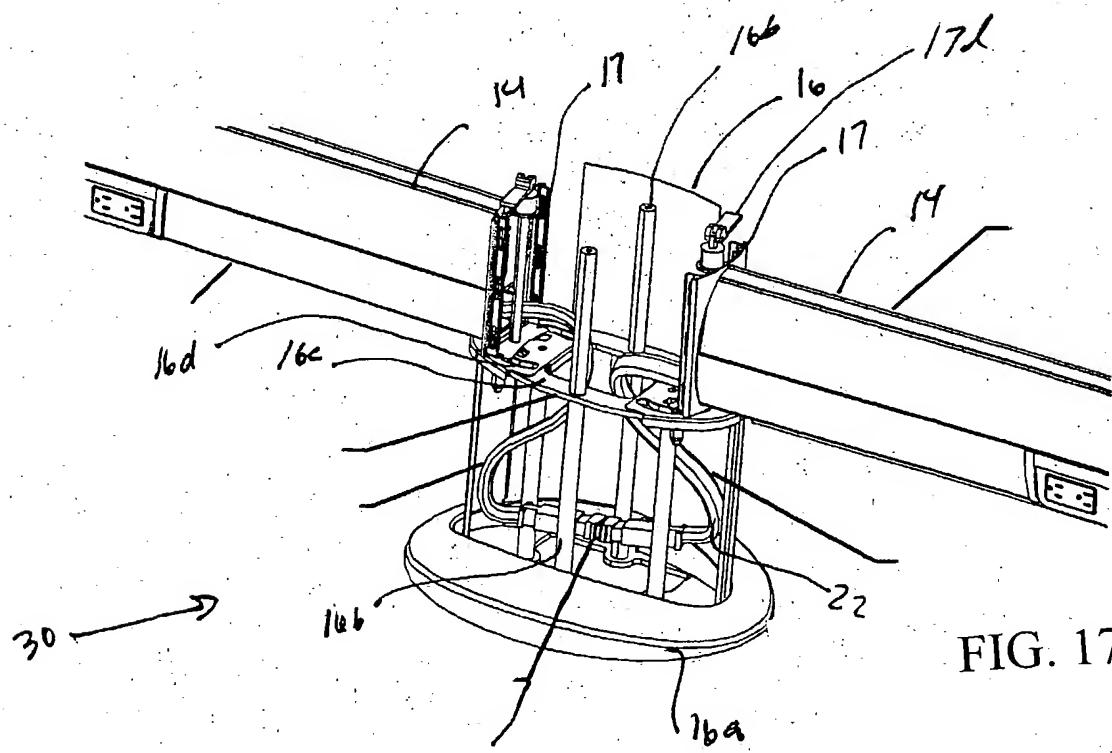


FIG. 17

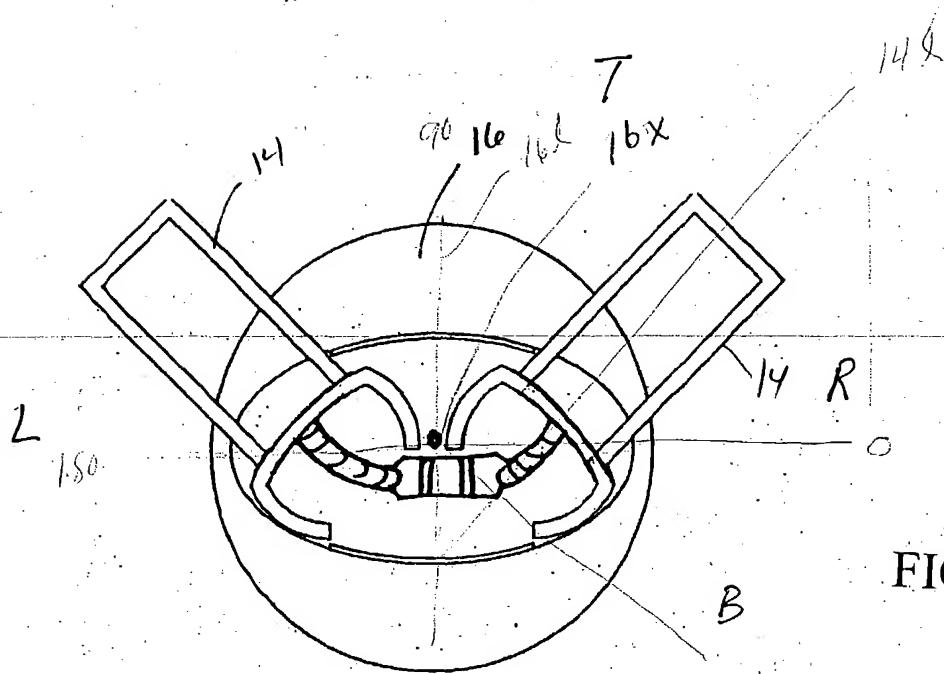


FIG. 18a

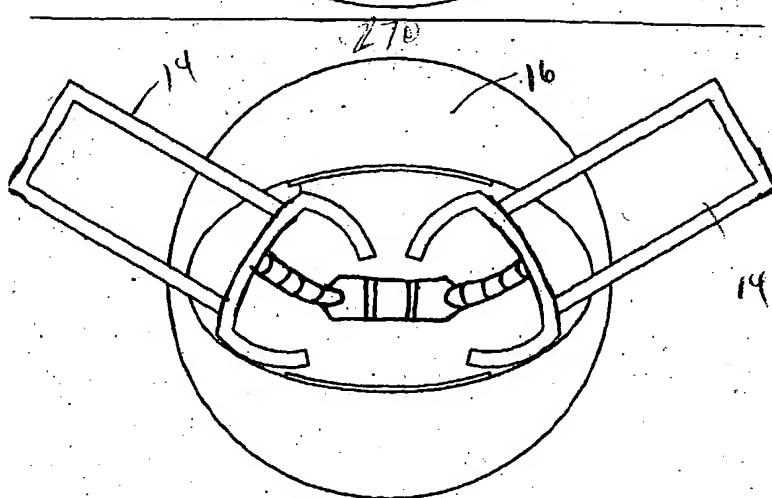


FIG. 18b

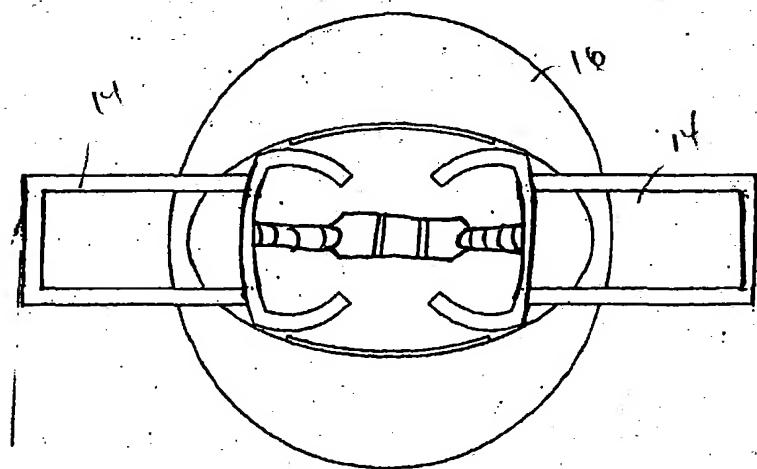


FIG. 18c

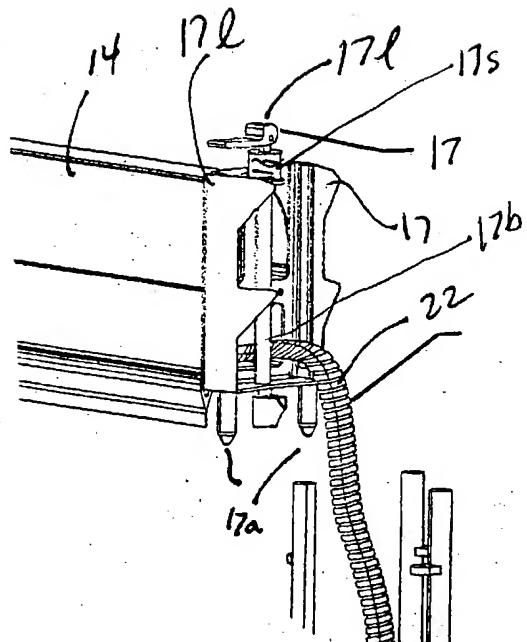


FIG. 19

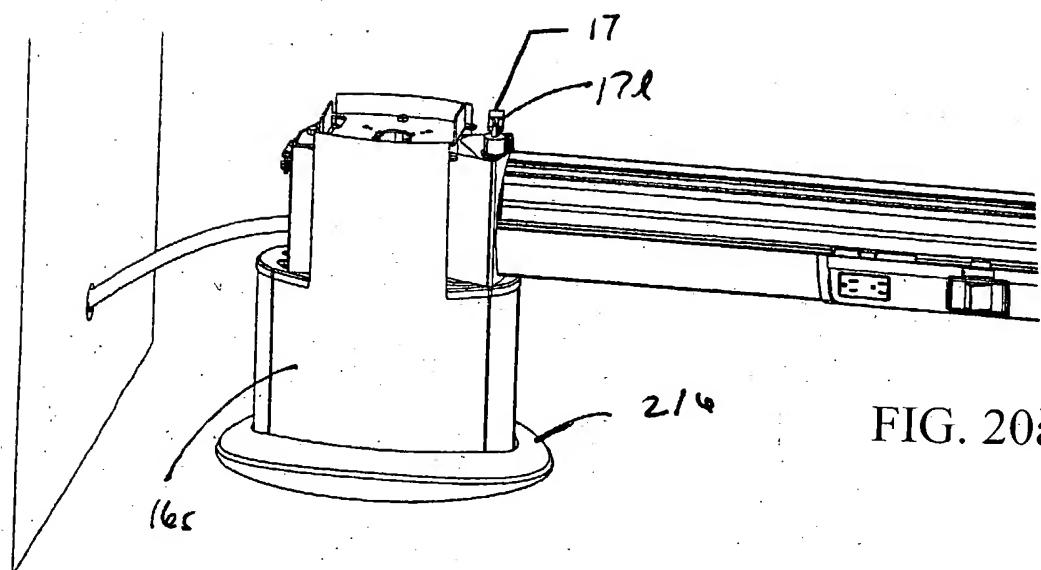


FIG. 20a

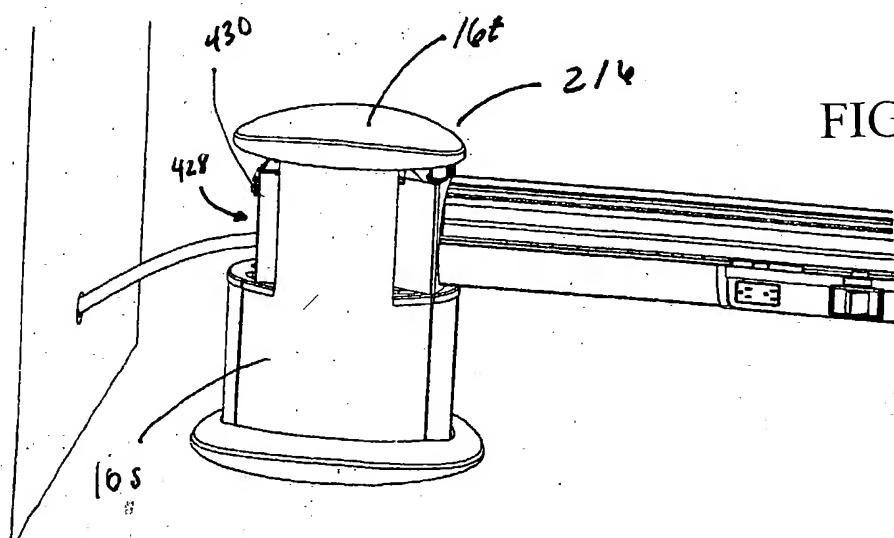


FIG. 20b

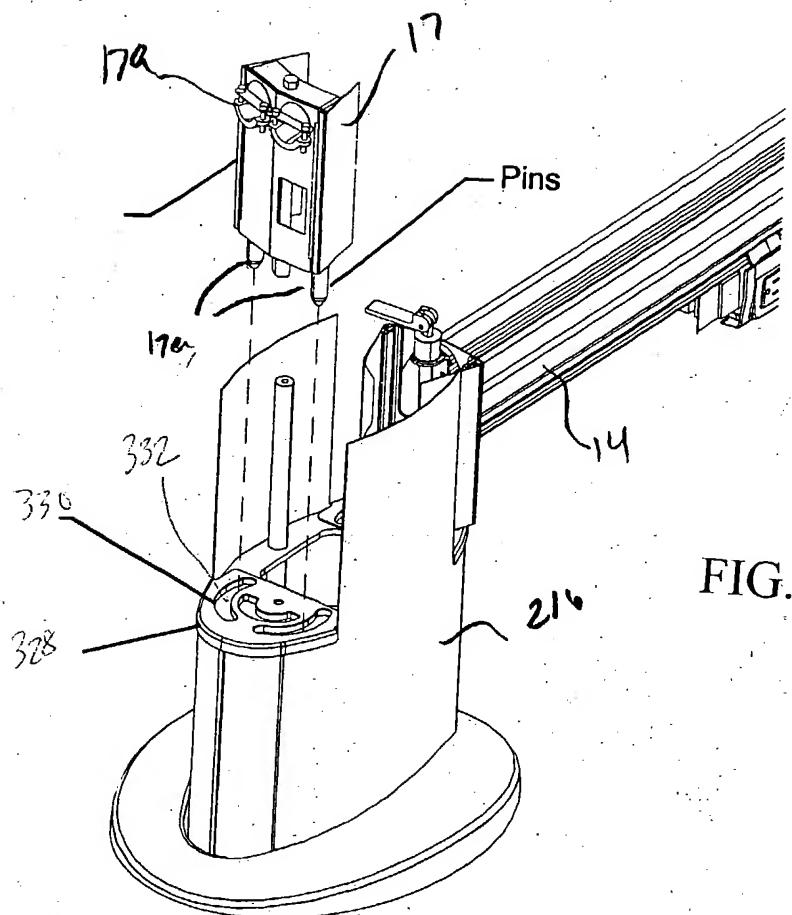


FIG. 20c

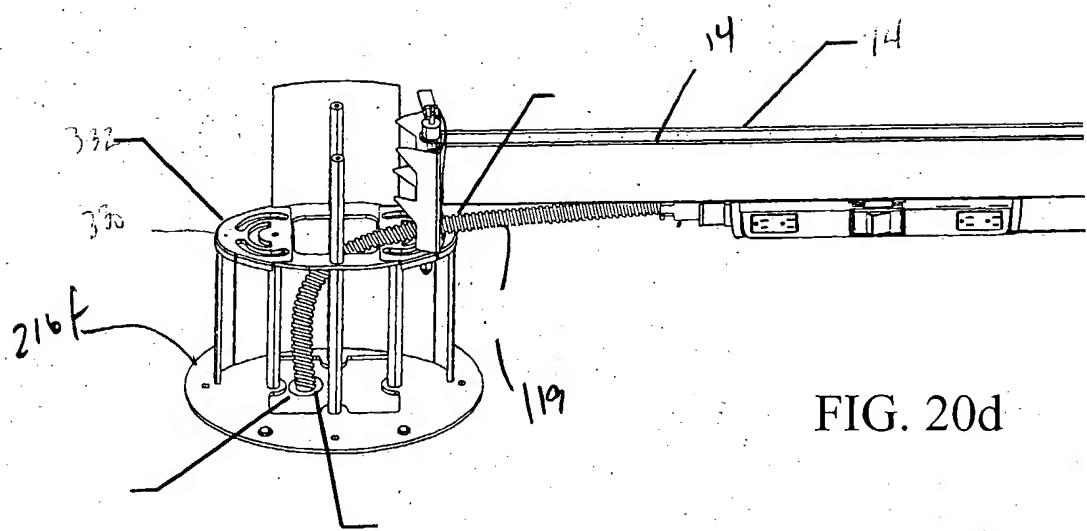


FIG. 20d

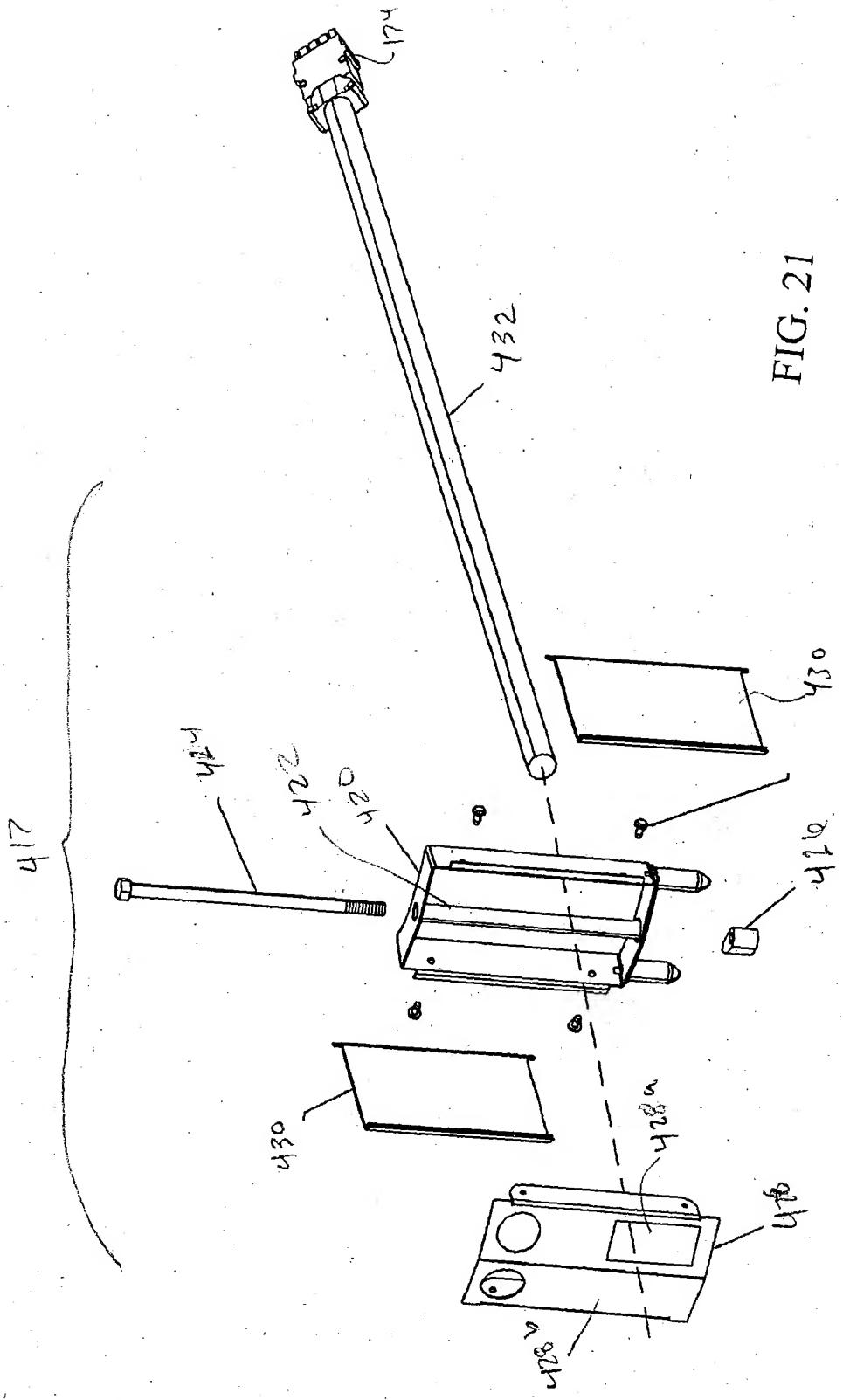
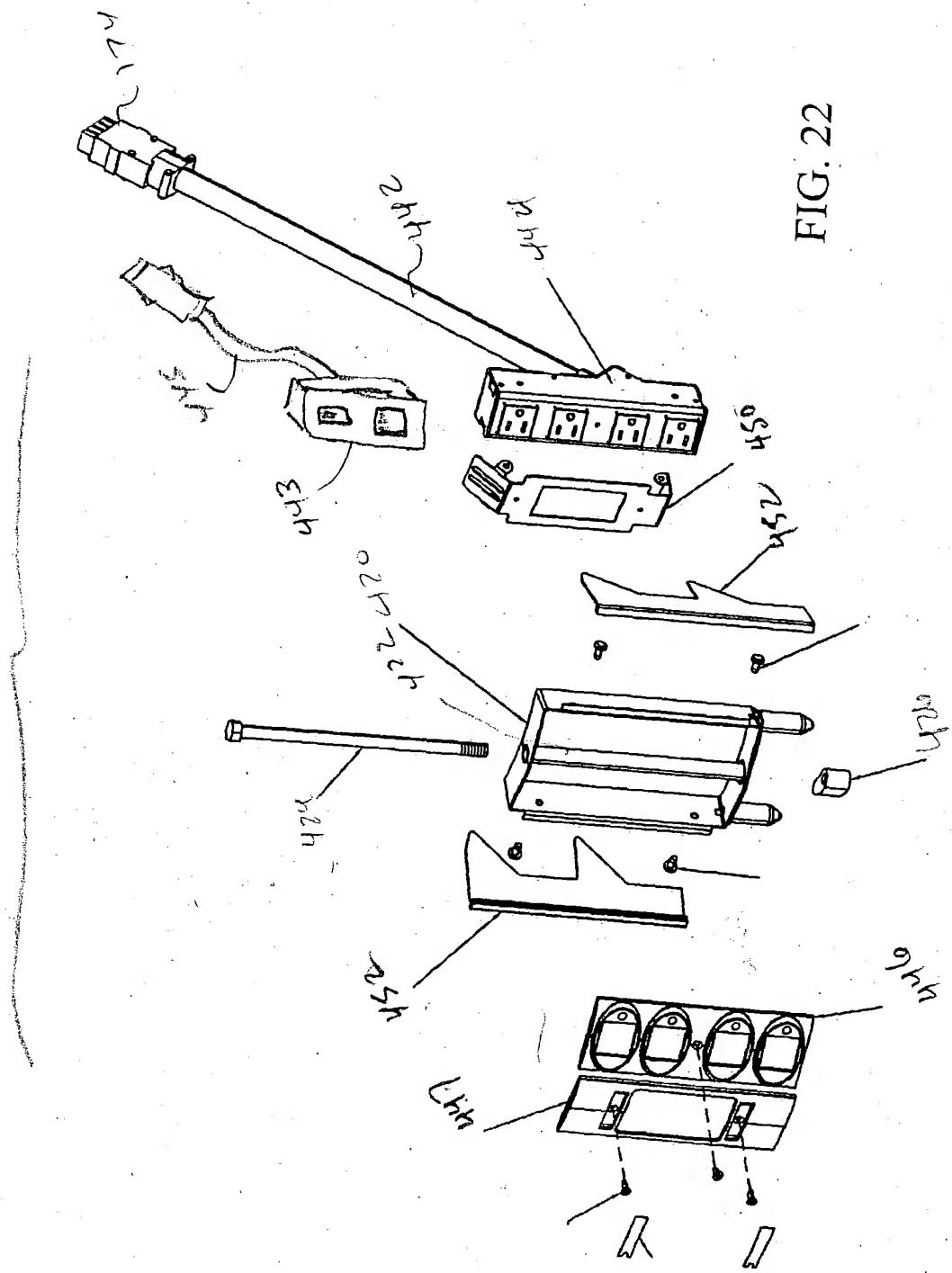


FIG. 21

22

FIG. 22



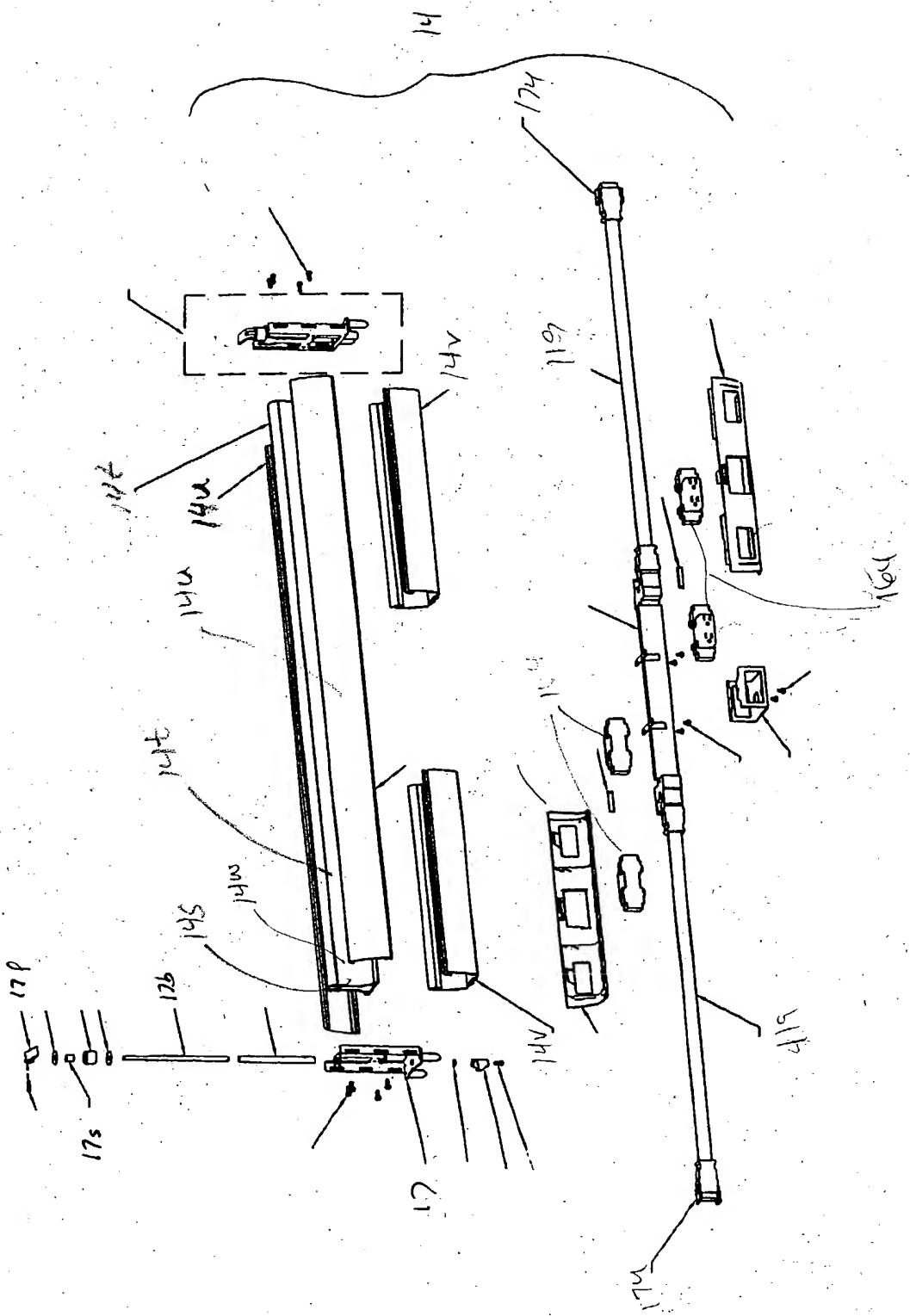


FIG. 23

FIG. 23a

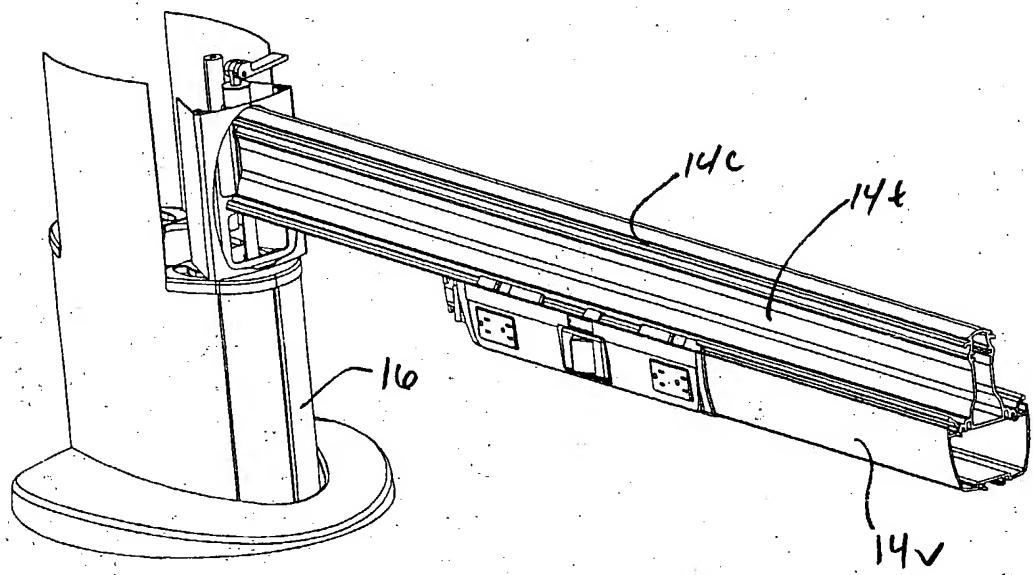
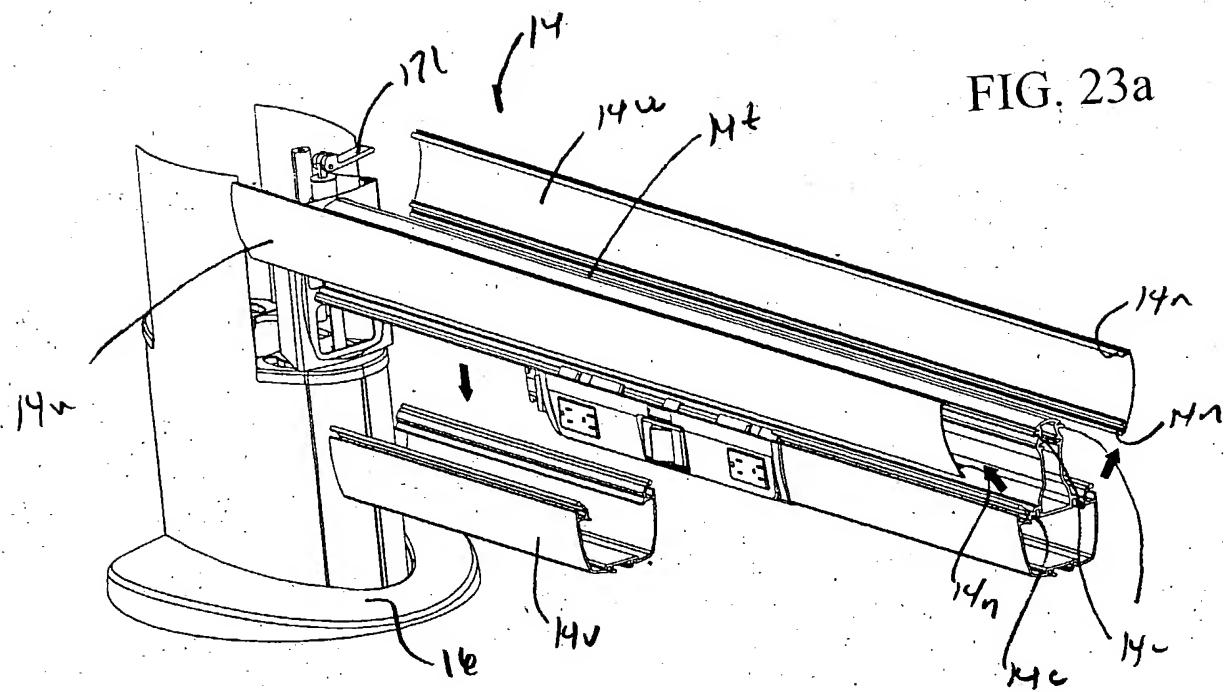


FIG. 23b

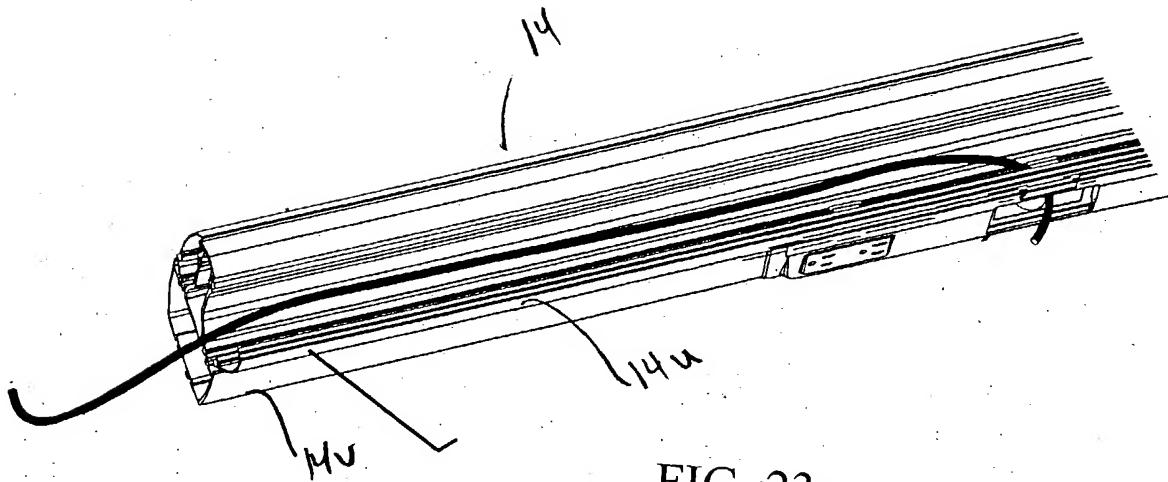


FIG. 23c

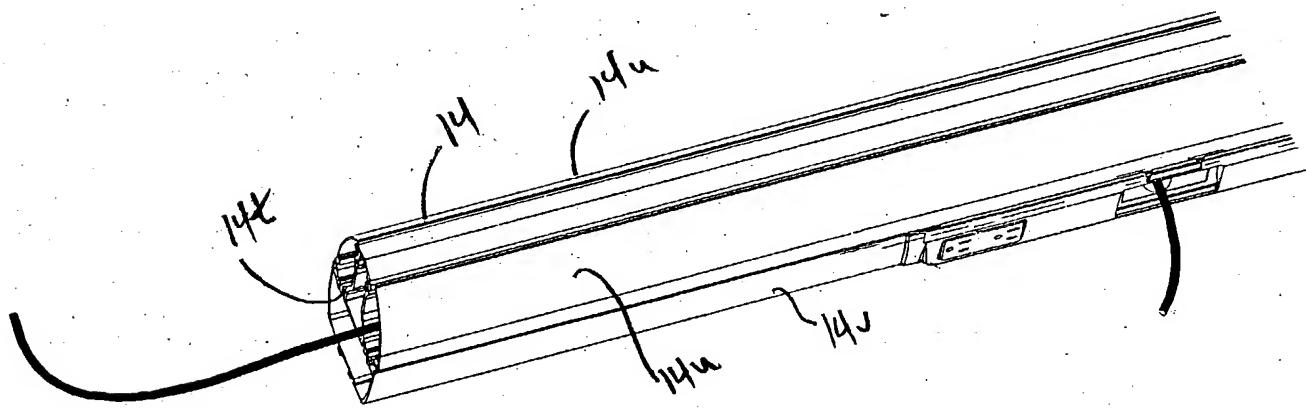


FIG. 23d

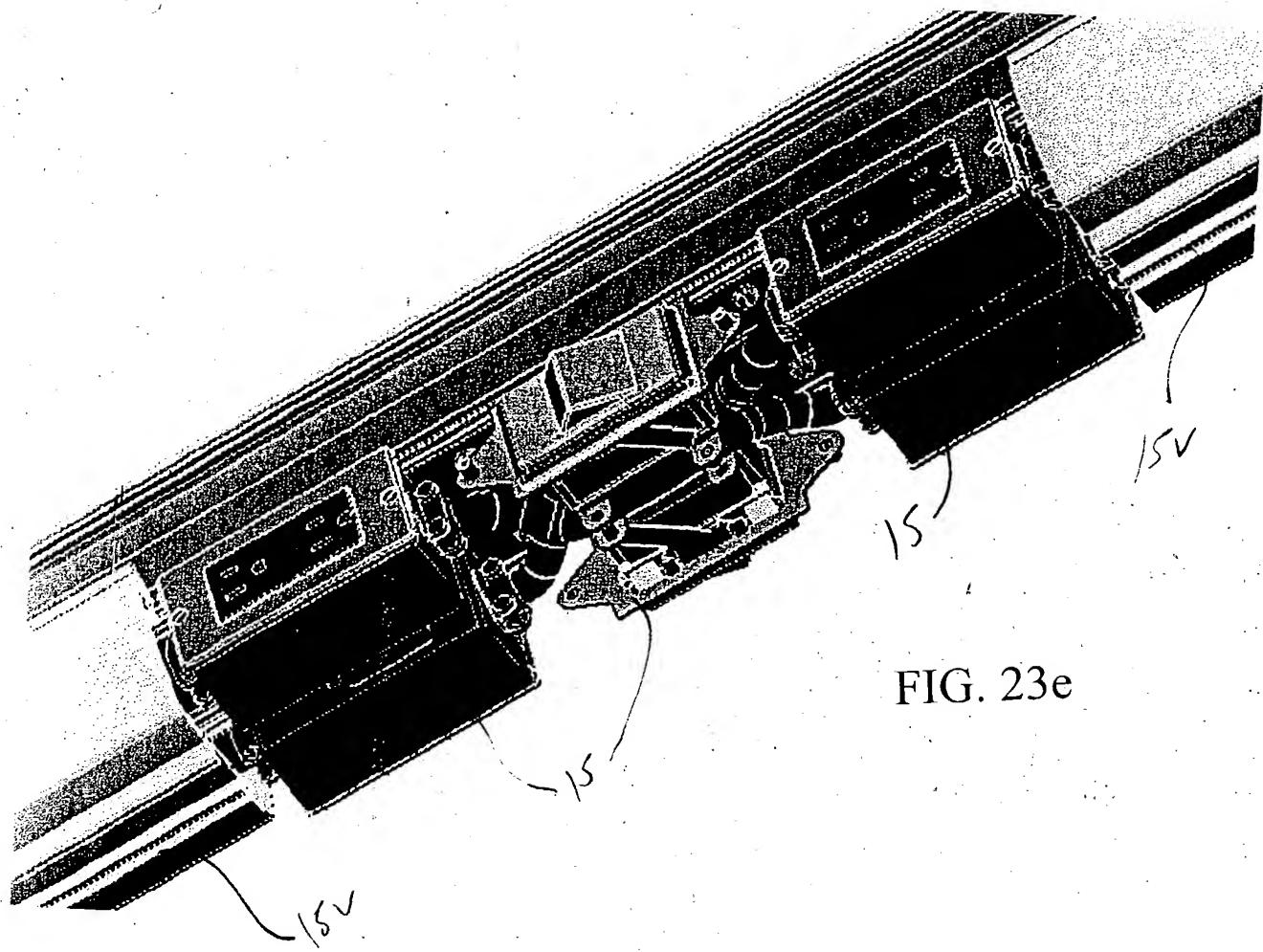
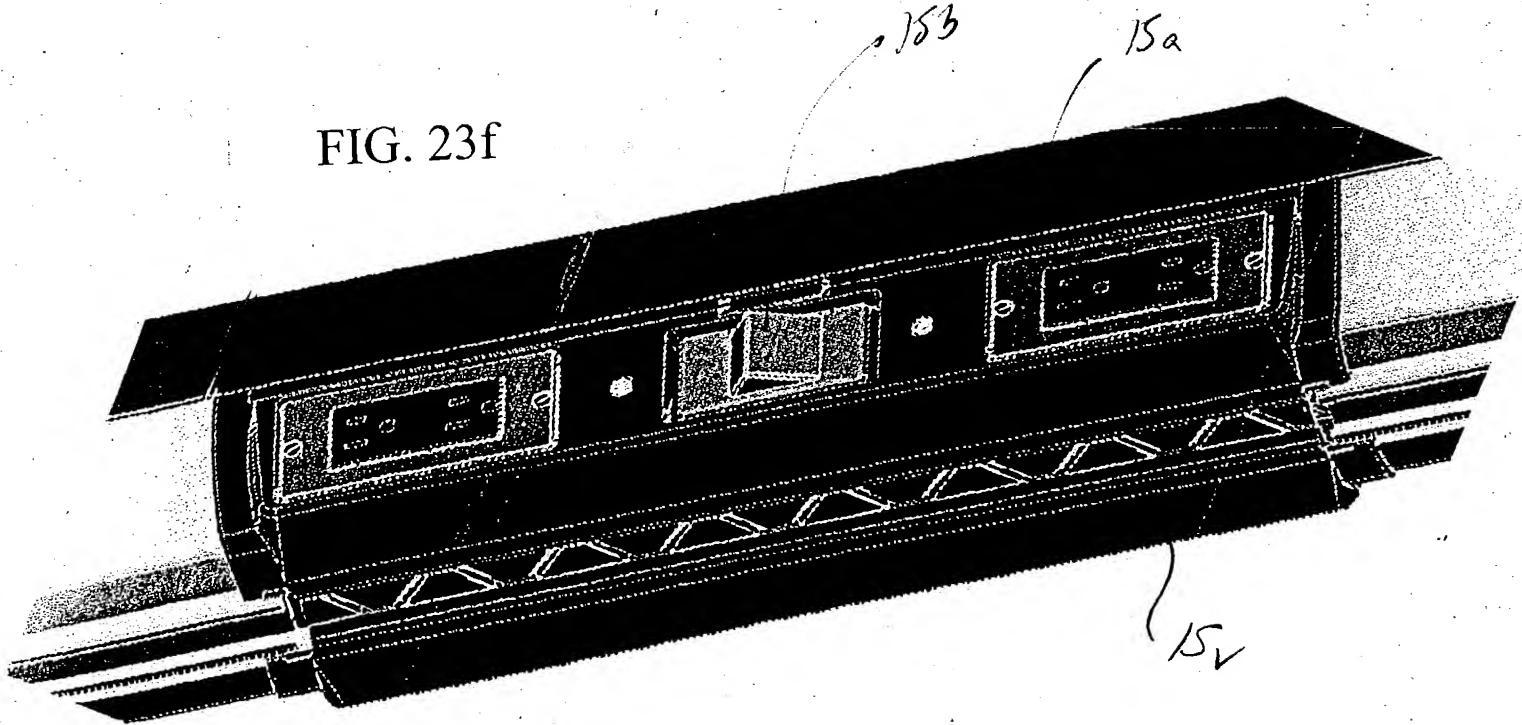


FIG. 23f



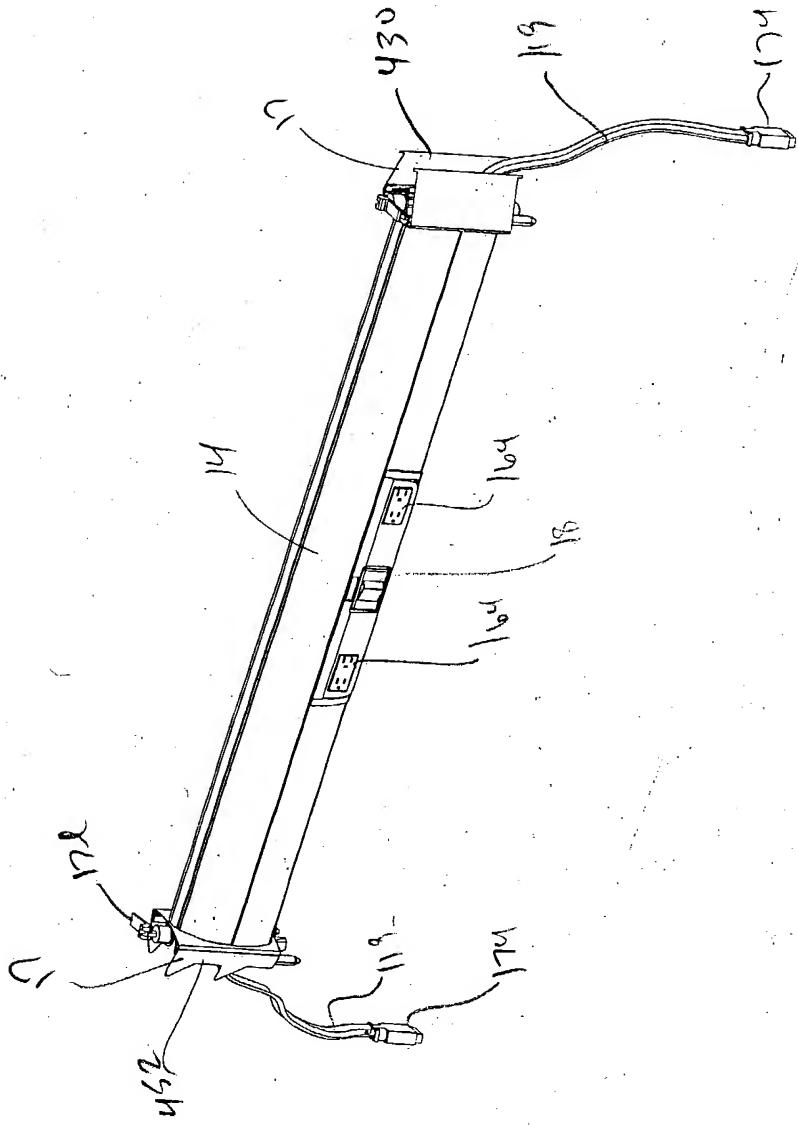


FIG. 24

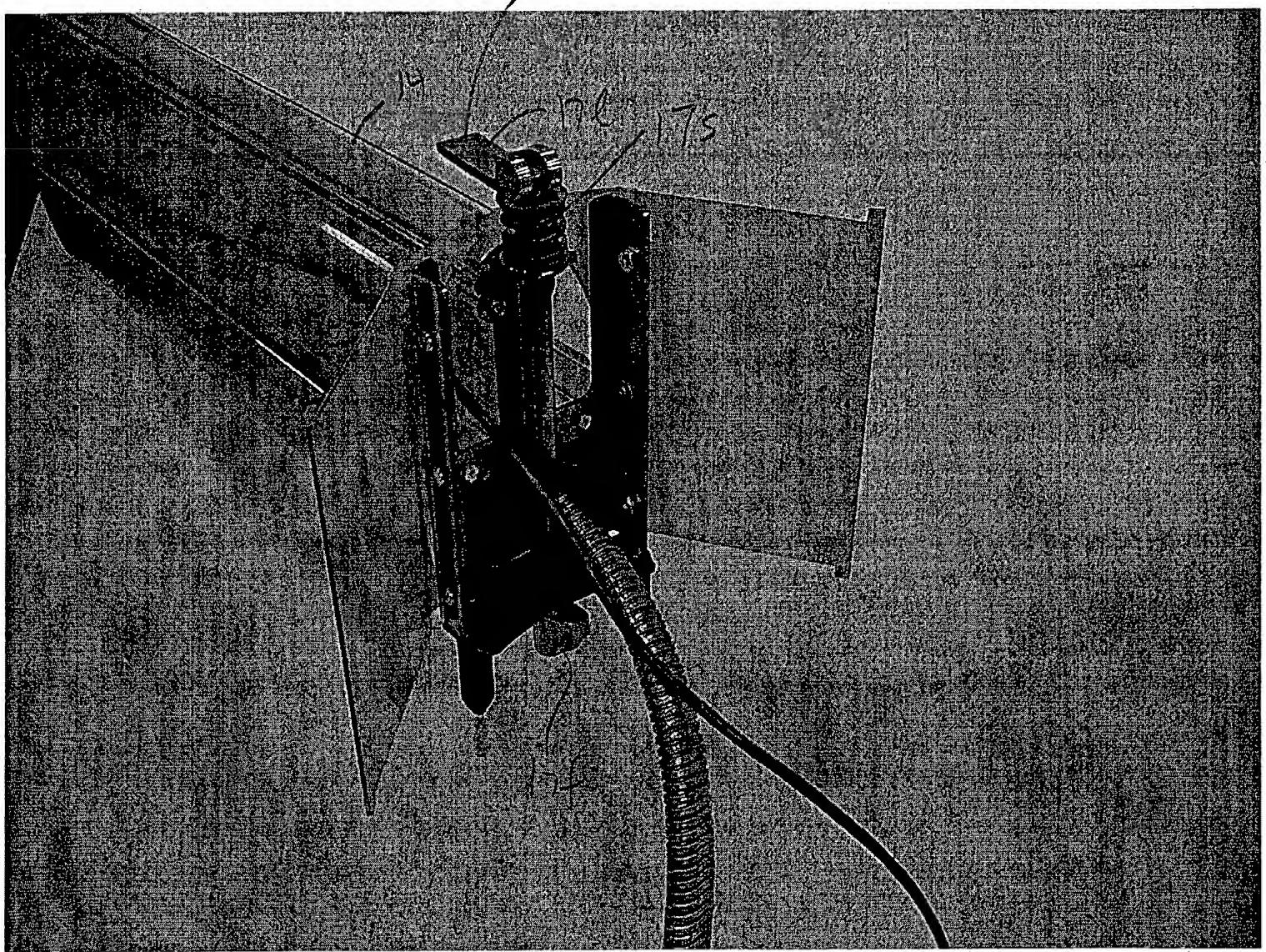


FIG. 24a

FIG. 25a

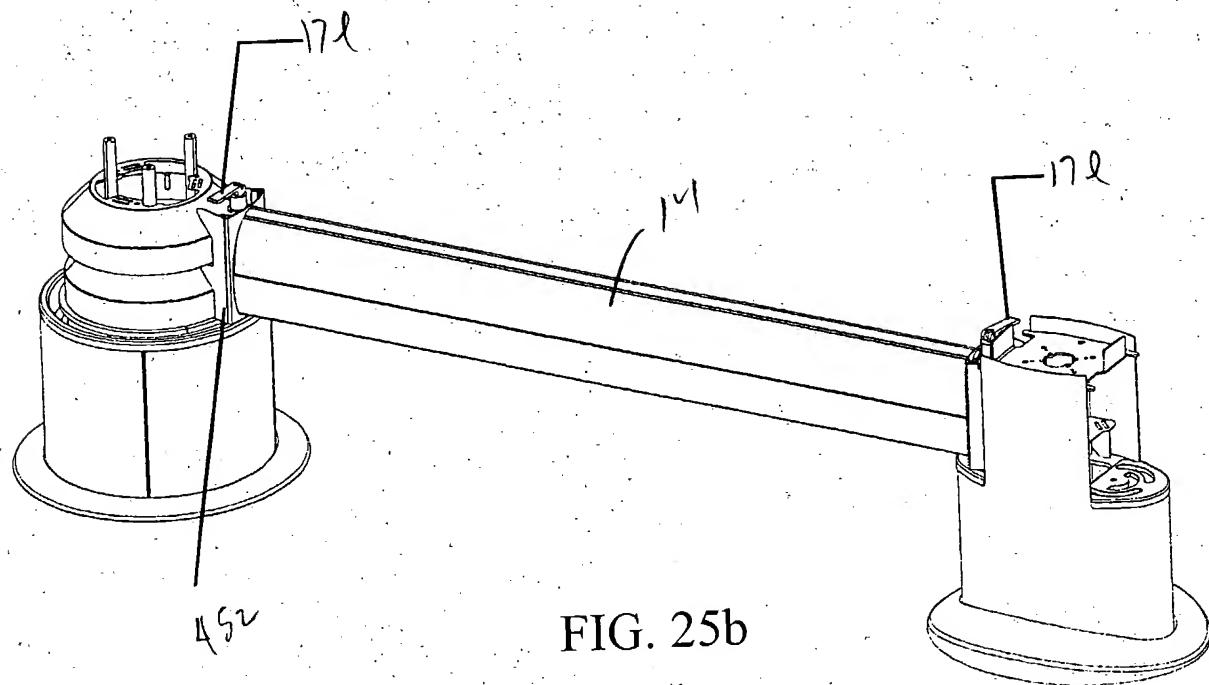
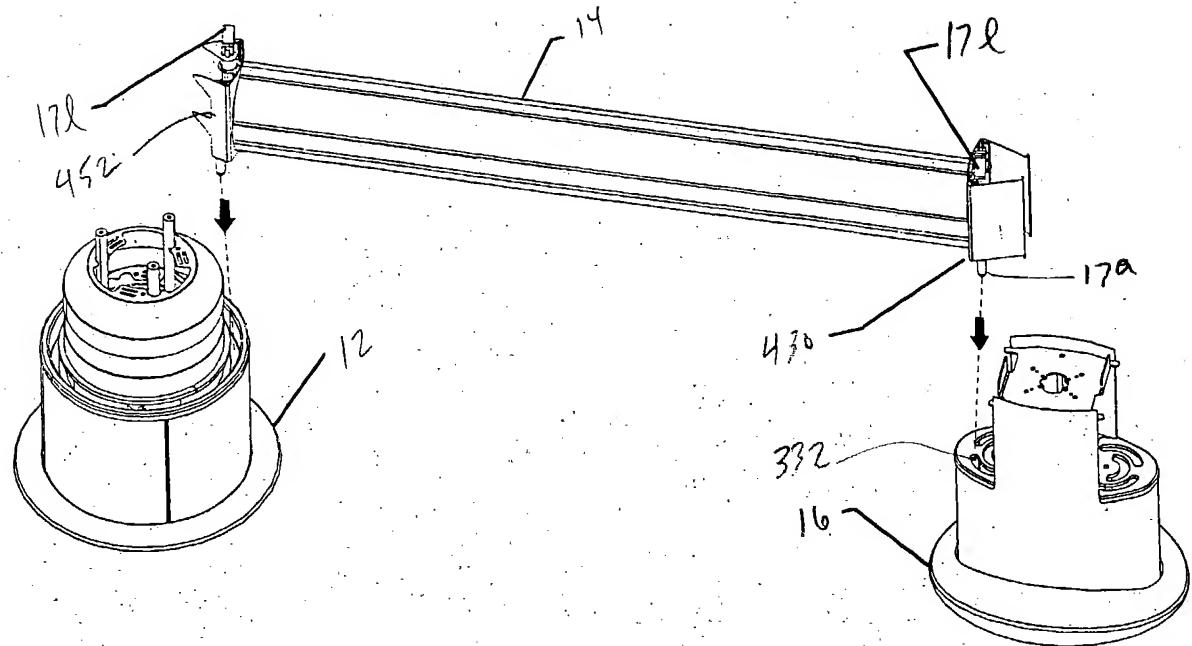
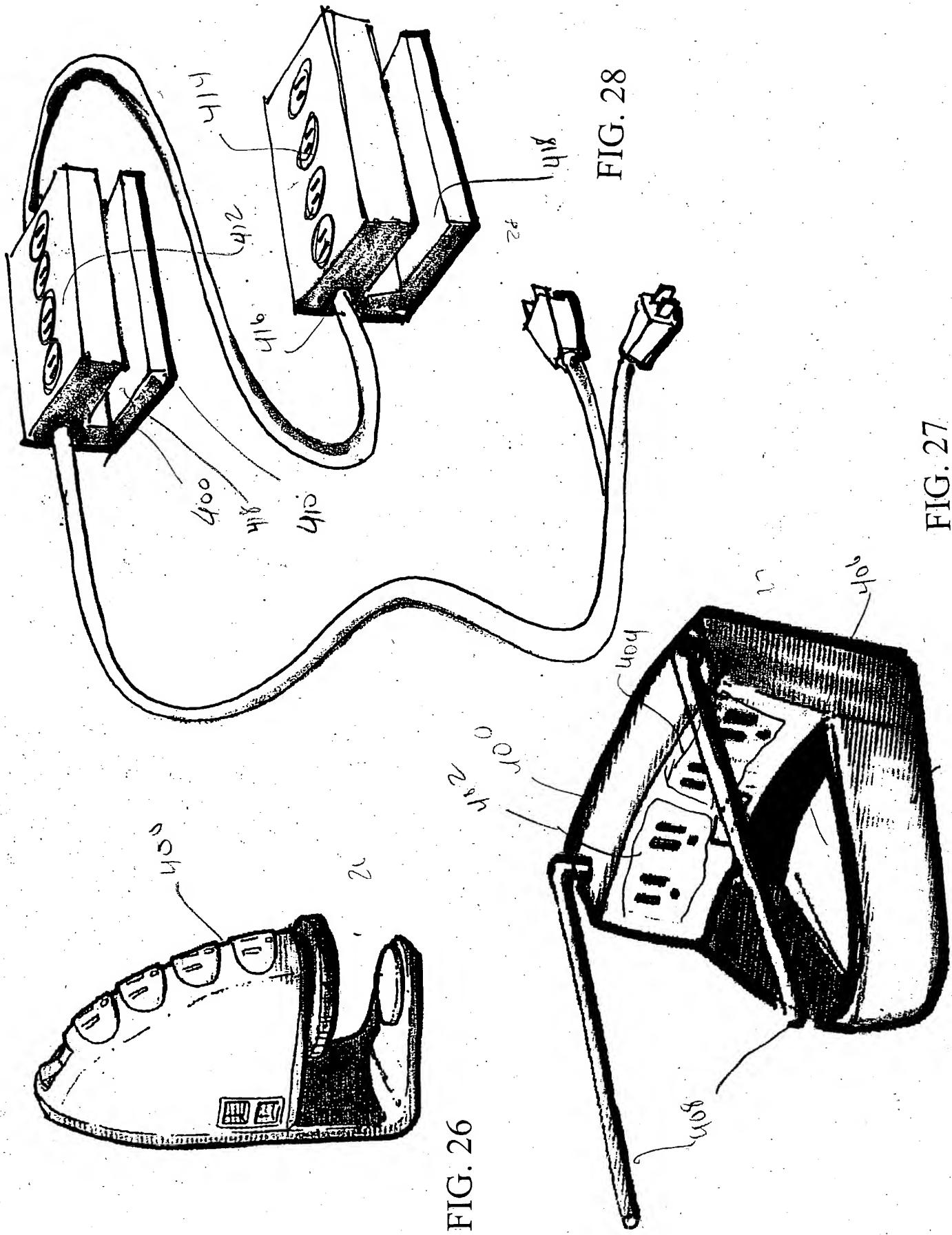


FIG. 25b

25b



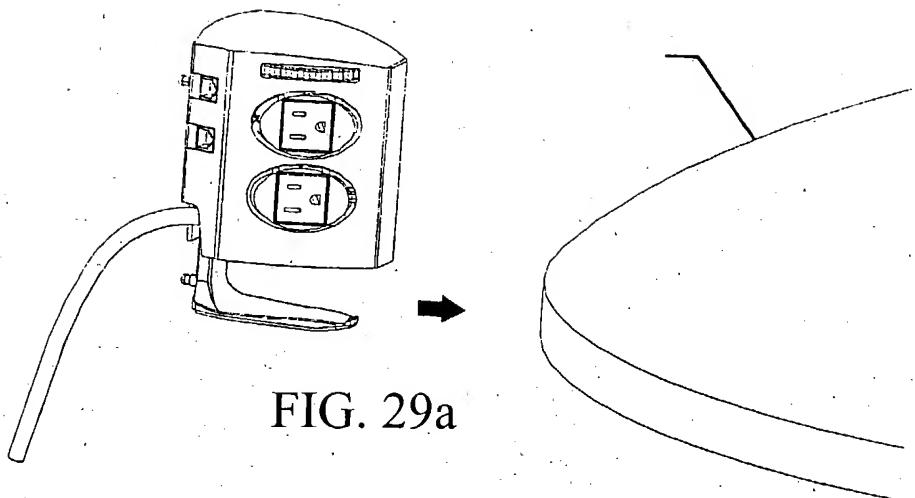
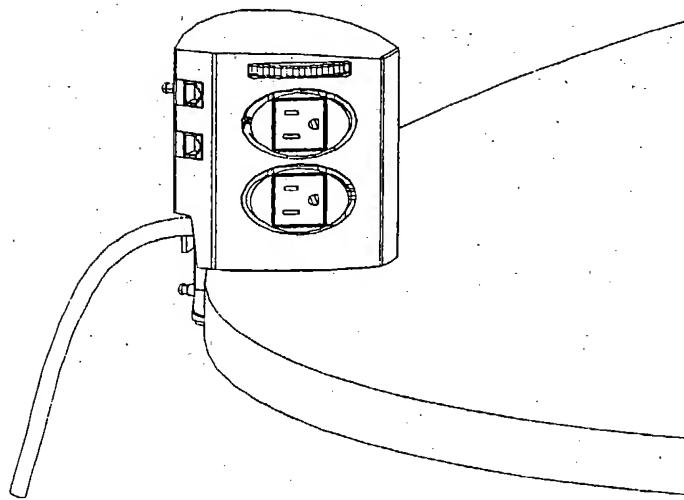


FIG. 29a

FIG. 29b



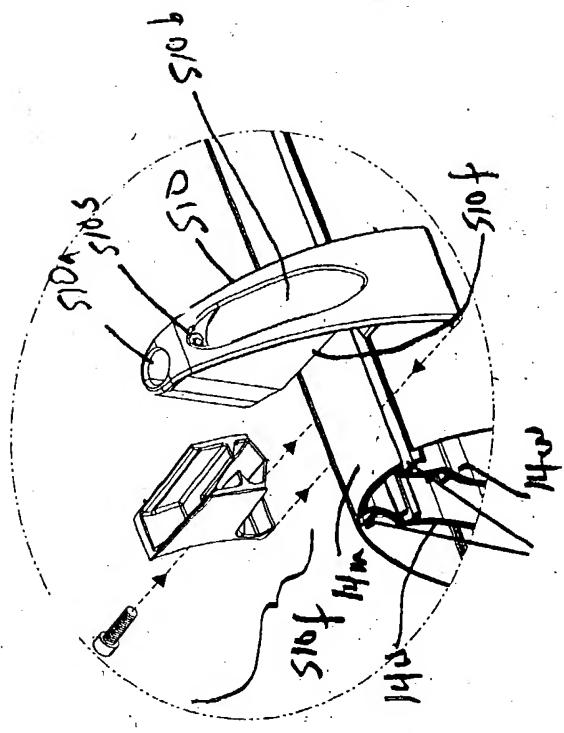


FIG. 30a

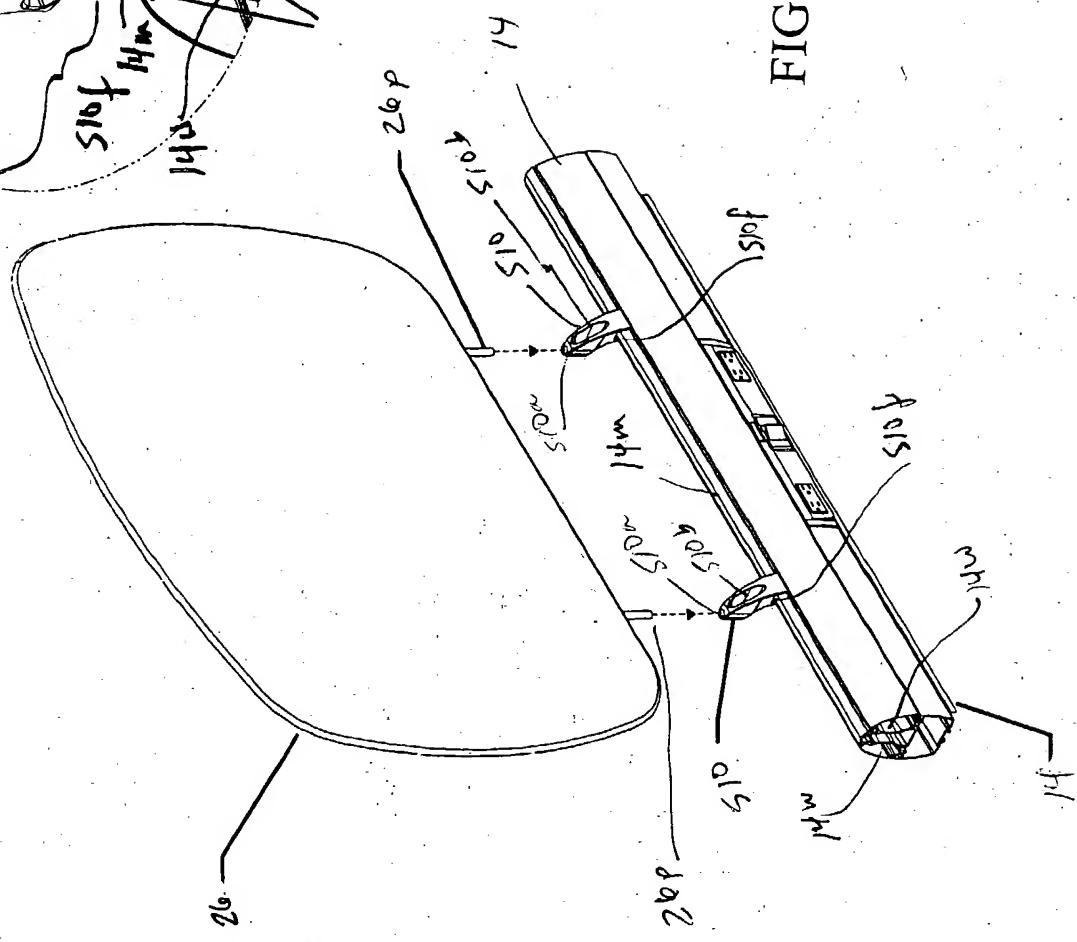


FIG. 30

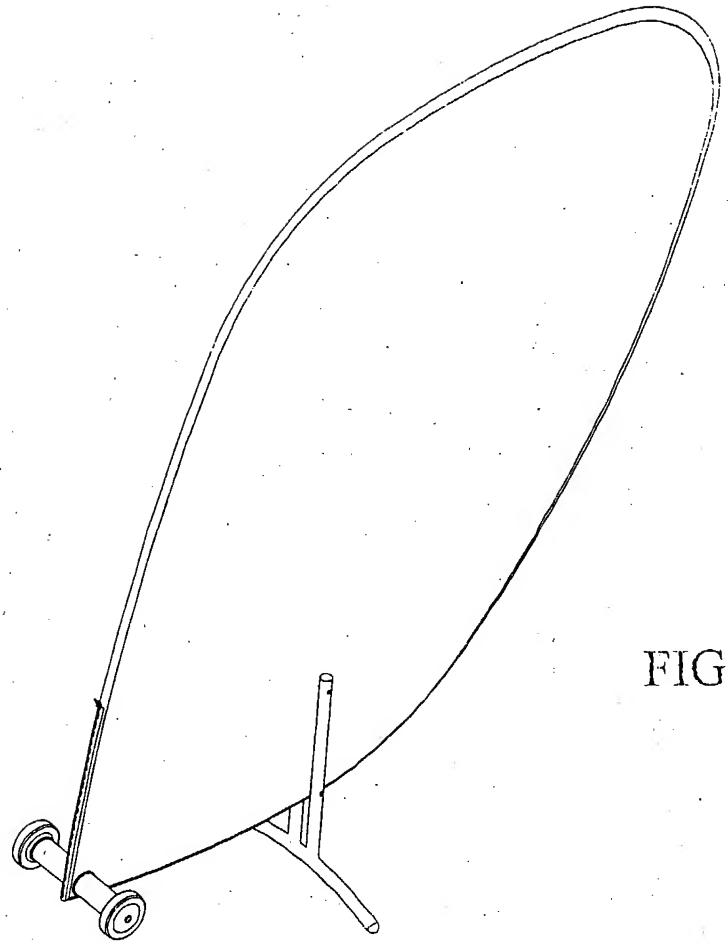
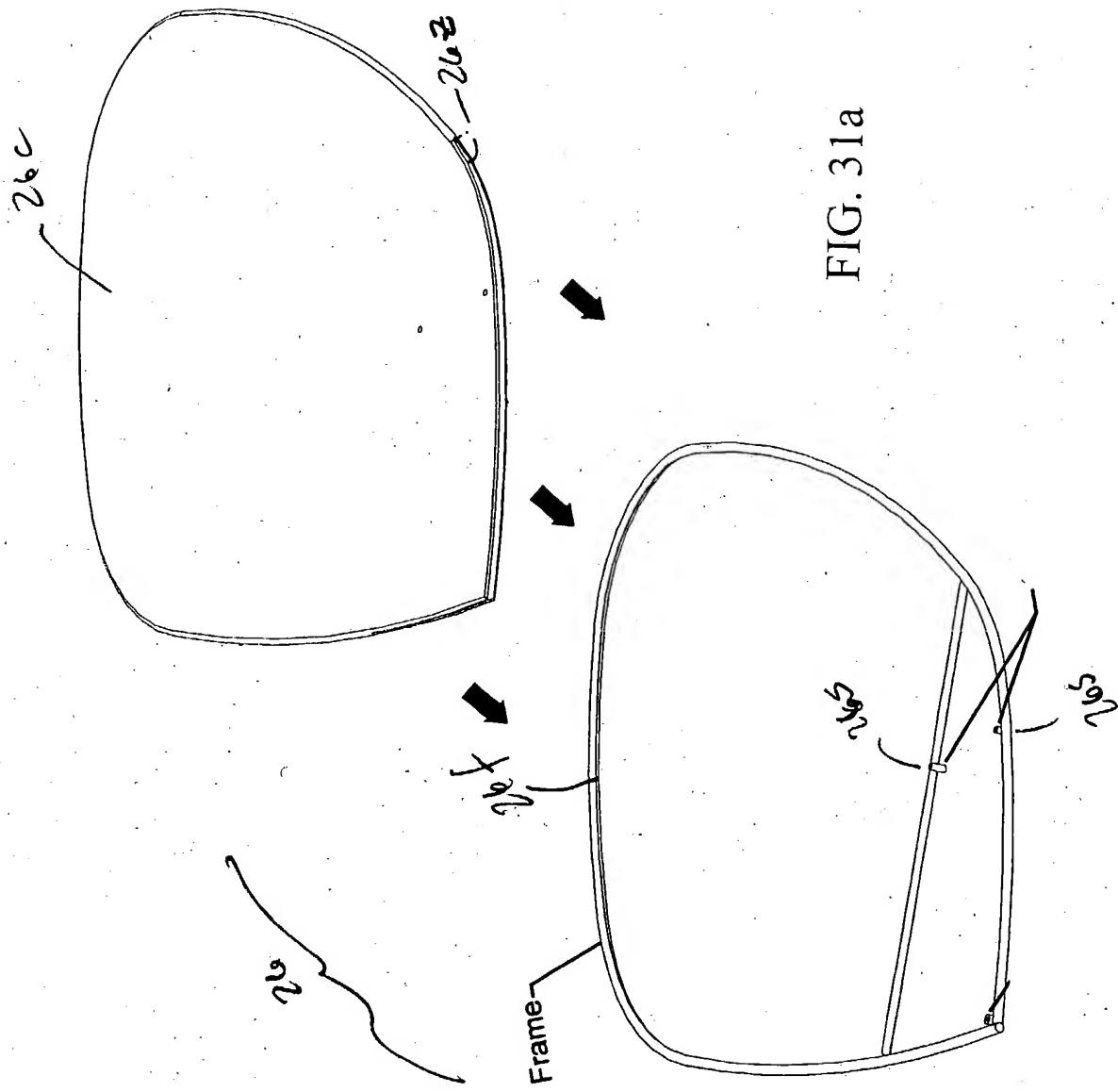


FIG. 31

FIG. 31a



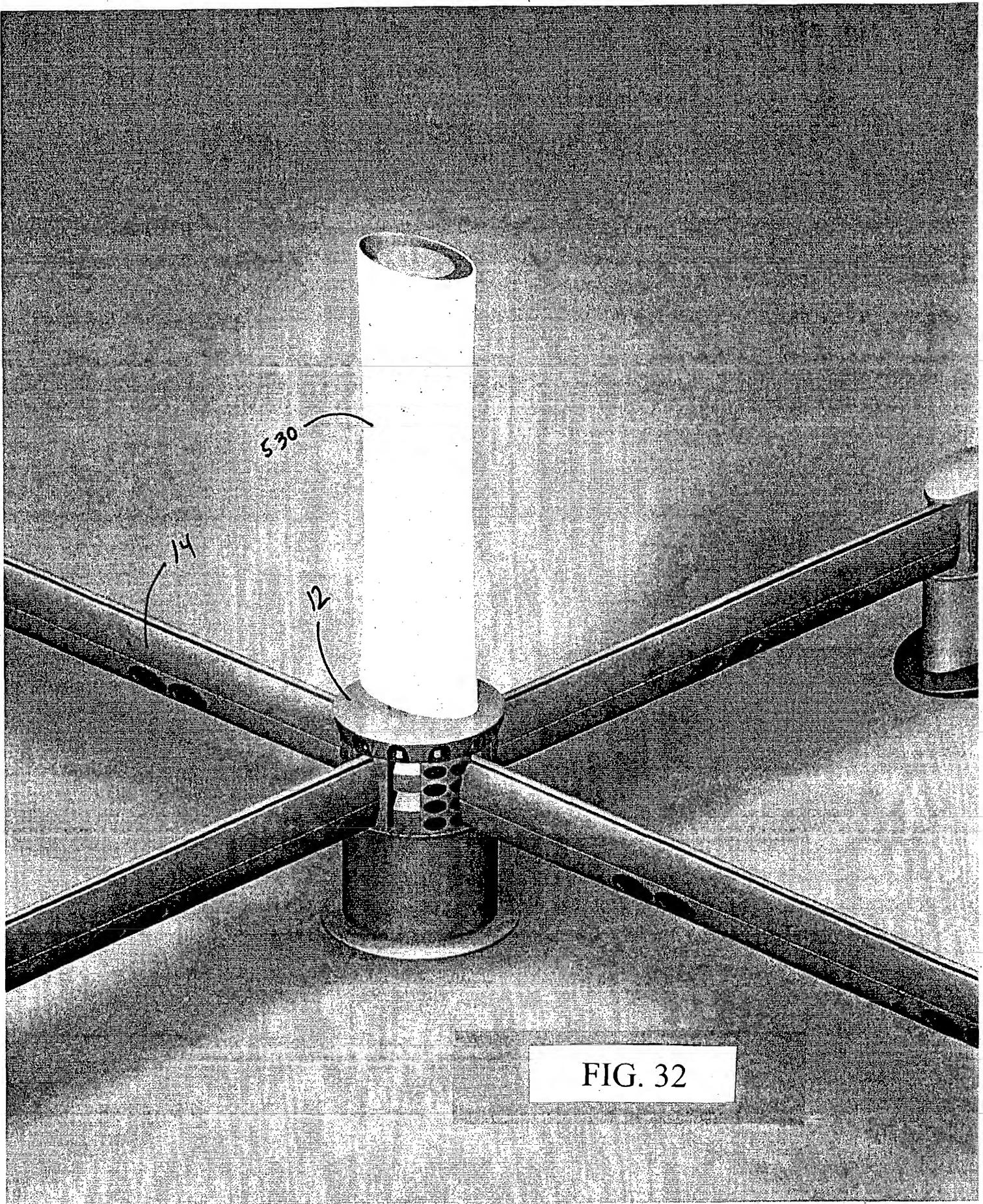


FIG. 32

FIG. 33a

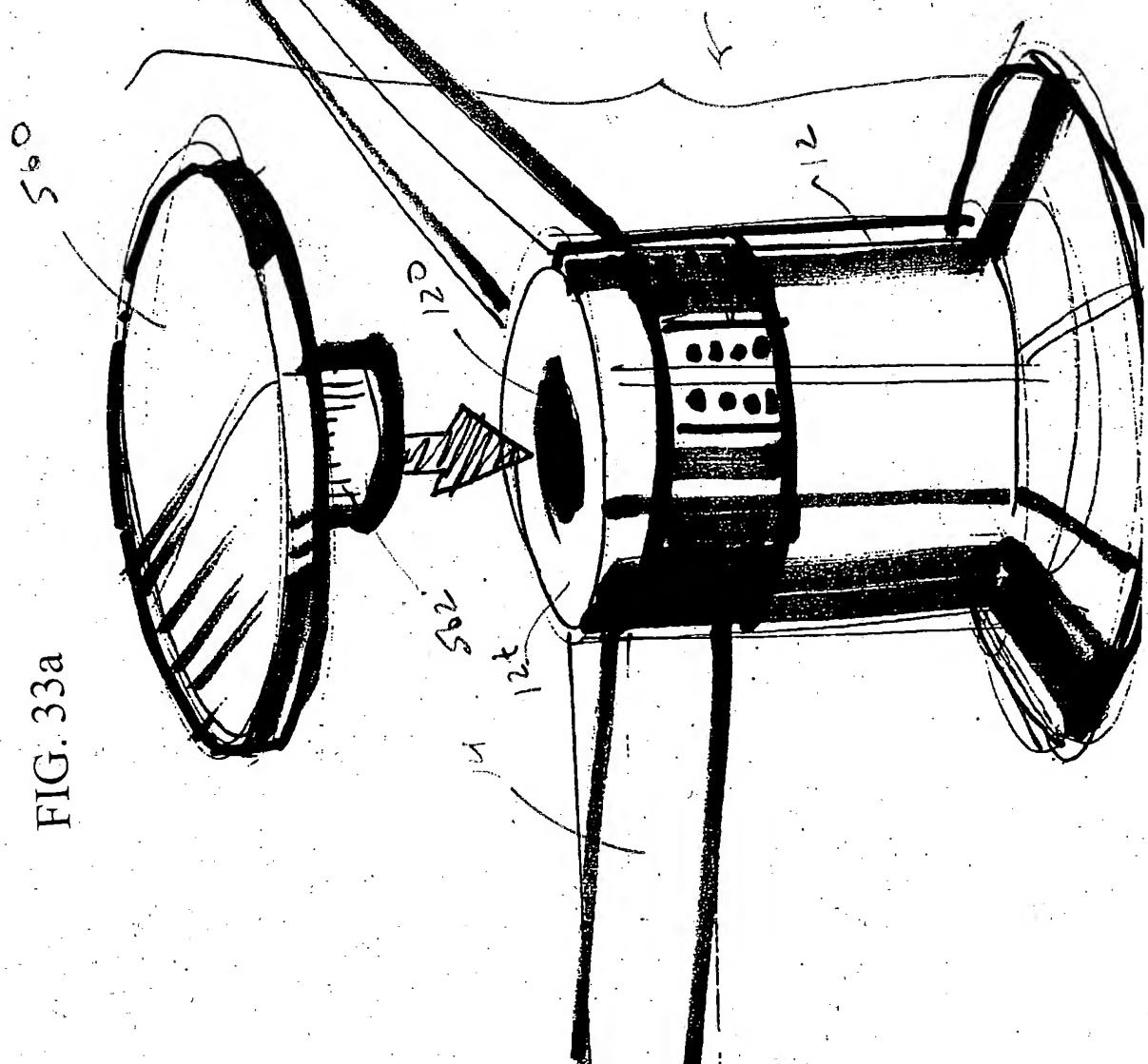
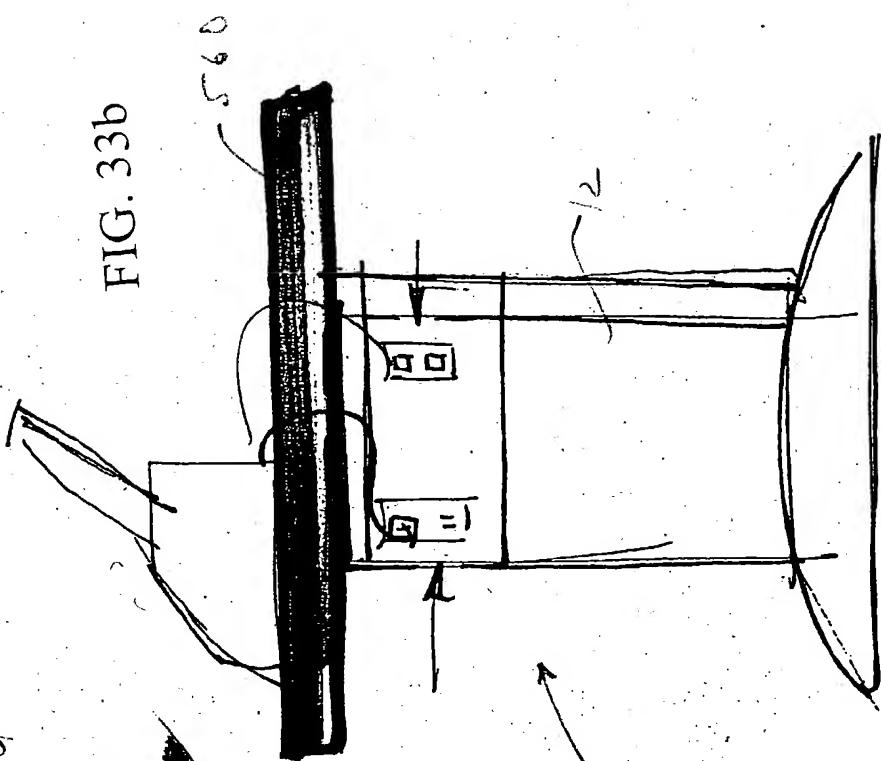


FIG. 33b



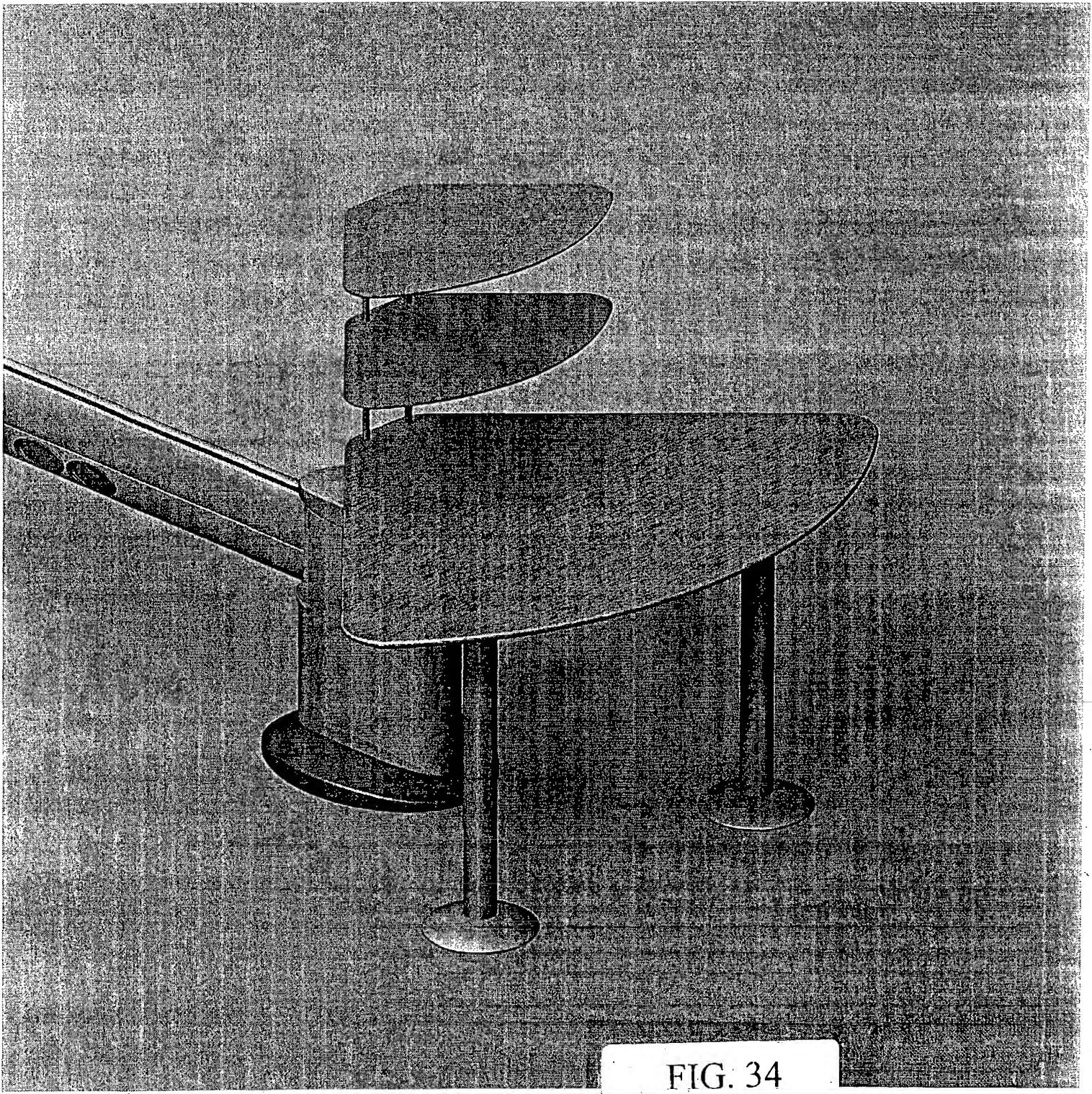


FIG. 34

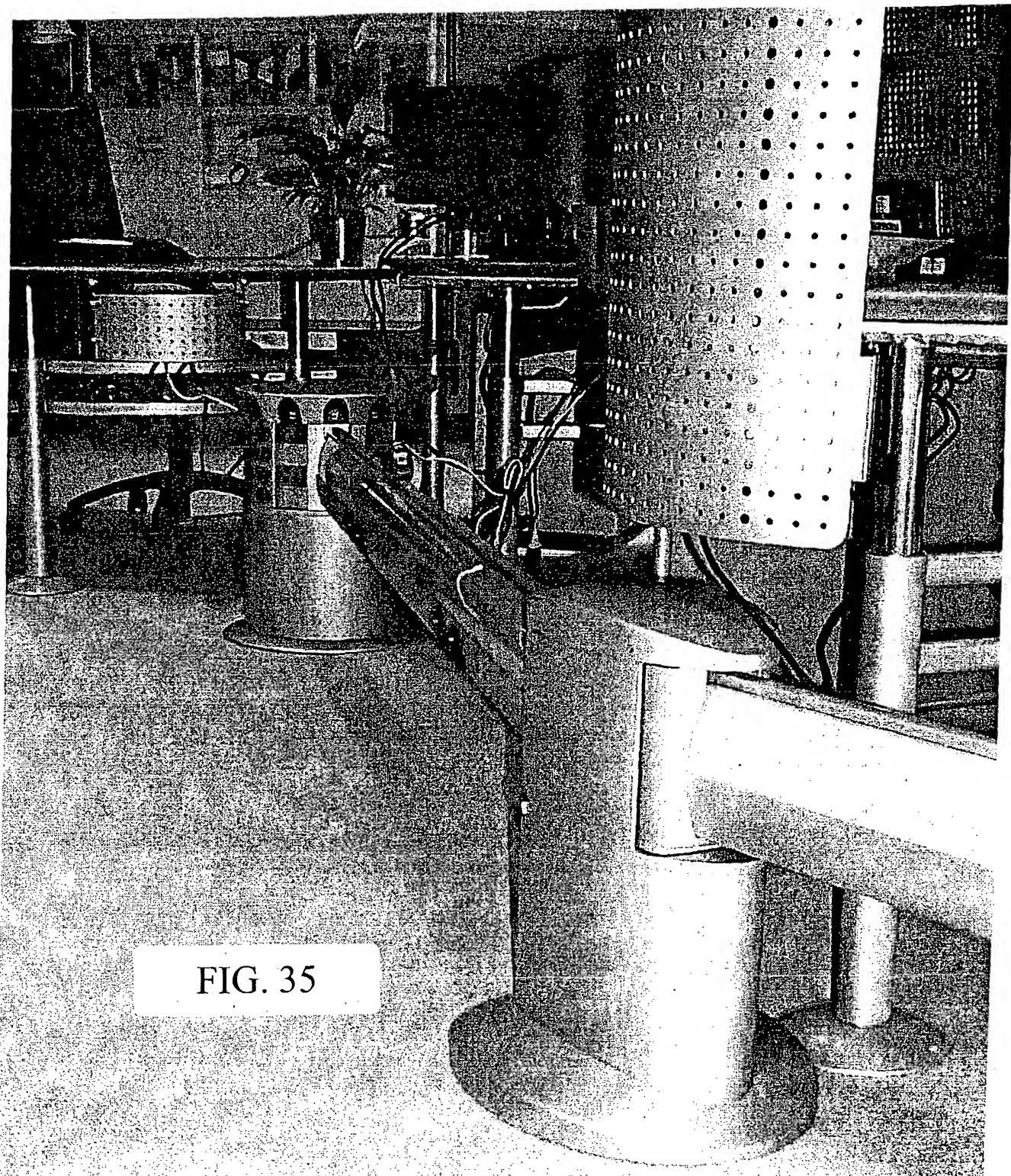


FIG. 35